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The ECCE 2021 Organizing Committee would like to express its gratitude for the generous support received from the following:

**GOLD SPONSOR**

STMicroelectronics

**SILVER SPONSOR**

OPAL-RT Technologies

**INDUSTRY SPONSOR**

GMW Associates
Welcome from General Chair

On behalf of the entire ECCE 2021 Organizing Committee it is my pleasure to welcome you to the 13th Energy Conversion Congress and Expo, ECCE 2021, co-sponsored by the IEEE Industry Applications Society (IAS) and IEEE Power Electronics Society (PELS). Joining IAS and PELS this year are STMicroelectronics as a Gold Sponsor and OPAL-RT as a Silver Sponsor. Please visit their booths in the user-friendly virtual exhibit, where you’ll also find many other companies which will provide you with information on their newest products and solutions.

Since its debut in 2009, ECCE has become the key annual event to attend if you, like me, are passionate about electrical and electromechanical energy conversion technology and if you enjoy sharing your ideas with other engineers, students and researchers from industry and academia. In the spirit of worldwide inclusion, ECCE 2021 is virtual, on a user-friendly platform, and with a plethora of events focused on giving you the opportunity to learn, network and discover what’s new and trending in integrated energy systems as well as individual energy conversion components.

ECCE 2021’s program features four live plenary talks by experts and leaders from Tesla, MagniX, General Electric Aviation, and Intel. They will present the latest energy conversion technologies and their visions for future research and development. Adopting the same live format as the plenary session, several special sessions will provide you with opportunities to participate in panels with experts from industry and academia. The topics are very timely and important ranging from additive manufacturing for electric machines to wide bandgap devices, from power electronics to interface distributed energy resources to standard development, from energy access for all countries to cybersecurity, from power converters as grid-interface to simulations, controls and thermal design.

The tutorial program opens the conference on Sunday with several informative and engaging live sessions. Thanks to the virtual format, you can access the tutorial presentations a few days before Sunday, to get the most out of the live sessions with the tutorial presenters. The topics include grid-forming technology, wide bandgap devices, electric machine design, electric drives, solar photovoltaic and energy storage systems, design practices, electromagnetic interference, resilience, reliability and electro-thermal design.

At the core of ECCE 2021, is the regular technical program, with almost 900 technical papers and as many on-demand oral presentations, which deliver the latest advancements in energy conversion technology with focus both on components and systems. I hope you’ll enjoy interacting with the authors, at your own pace, through the chats available for each technical presentation throughout the conference. Two of these sessions are dedicated to Prof. Akira Nabae and Prof. Braham Ferreira, who recently passed away and were attendees of many ECCE conferences. They left their imprint on the field of energy conversion through their research contribution and through their many students; we will never forget them.

Following the tradition of the past few years, ECCE 2021 is co-located with the 2021 IAS Annual Meeting. We share the plenary session, the award presentations and the exhibit hall, which is free for everybody. If you are a student, you have access to both technical programs.

The ECCE 2021 program was created by an extraordinary and diverse team of volunteers, women and men from the industry, government laboratories, and academia who selflessly donated their time and talent to ECCE 2021 for almost two years. The contribution from IEEE personnel, SmithBucklin and Vfairs was also instrumental in organizing this conference. Thank you for making ECCE 2021 as engaging in virtual format as it has been in-person in past years.

I invite you to enjoy the 5 live days, and to also take advantage of the additional 25 days during which the on-demand content, including exhibitor materials, technical presentations, recorded live sessions and panels, product sessions, award presentations and student demonstrations, will be available on the virtual platform.

Thank you for participating in ECCE 2021. Together, we contribute to a more sustainable world!

Giovanna Oriti
ECCE 2021 General Chair
Welcome from Technical Program Committee

On behalf of the Technical Program Committee it is our great pleasure to welcome you to the IEEE Energy Conversion Congress & Exposition (ECCE 2021). We were hoping to resume the in-person meeting this year or to have a hybrid conference at the very least. Unfortunately, this turned out to be impossible, but we hope that the virtual mode meets your expectations. We created an exciting program for all virtual attendees with live events such as tutorials, special sessions, plenary sessions and the expo, and we combined it with prerecorded videos for the rest of the technical program.

The world’s leading conference on energy conversion systems, ECCE, is sponsored by the IEEE Industry Applications Society (IAS) and IEEE Power Electronics Society (PELS). It offers a multidisciplinary array of topics, ranging from devices to electrical power systems. Almost 900 technical presentations are organized into thematic sessions, with offline Q&A that authors will monitor daily. The material of the conference will be available for 30 days for attendees.

The ECCE Technical Program Committee managed a larger team this year consisting of 27 track chairs and 179 topic chairs who organized and supervised the review process to identify the best papers for ECCE. We had two plenary virtual meetings in April to finalize all paper decisions, and more in July and September to decide on the final program. The conference received a total of 1576 digest submissions from 62 countries. After a rigorous peer review and scheduling process, 892 papers are scheduled for presentation. On average, each digest received over 4 reviews. Our gratitude goes to the valued track chairs, topic chairs and to the generous 1650 reviewers who made all this possible.

The tutorials co-chairs received and reviewed 50 proposals and selected 17 tutorials to offer online this year. We are also proud of offering 24 special sessions on various topics.

We look forward to virtually welcoming you to ECCE 2021—thank you for helping us keeping the ECCE community connected!

ECCE 2021 Technical Program Committee

Jean-Luc Schanen
Michael Harke
Gianmario Pellegrino
Elisabetta Tedeschi
Mark Scott
Luca Zarri
2021 Organizing Committee

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Tradeshow Coordinator
Priscilla Gil  
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Emily Claussen  
Event Coordinator
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Nathan Weise, Marquette University, USA
Prabakar Kumaraguru, National Renewable Energy Laboratory, USA
Zachary Smith, University of Pittsburgh, USA
Hashim Al Hassan, Switched Source LLC, USA
Hamed Nademi, California State University, San Marcos, USA
Xu She, Carrier Corporation, USA
Tiefu Zhao, The University of North Carolina at Charlotte, USA
Ryan Brody, University of Pittsburgh, USA
Zhe Zhang, Eaton Corporation, USA
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Liang Du, Temple University, USA
Josue Campos do Prado, Washington State University Vancouver, Canada

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Miroslav Vasic, Universidad Politecnica de Madrid, Spain

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Subrata Saha, Aisin AW Co. Ltd., Japan
Hassan Eldeeb, BorgWarner Inc., USA
Xiaofeng Yang, Beijing Jiaotong University, China
Tao Yang, University of Nottingham, UK
Qi Li, Southwest Jiaotong University, China
Rui Ma, Northwestern Polytechnical University, China
Arnaud Gaillard, University of Technology of Belfort-Montbéliard, France

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Dong Cao, University of Dayton, USA
Hengzhao Yang, ShanghaiTech University, China
Hidemine Obara, Yokohama National University, Japan
Damian Oslebo, Naval Sea Systems Command (NAVSEA), USA
Samantha Gunter, General Motors, USA
Ali Khajehoddin, University of Alberta, Canada
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Vice Chair: Daniel Costinett, University of Tennessee, Knoxville, USA

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Saad Pervaiz, Texas Instruments, USA
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Xibo Yuan, University of Bristol, England
Mahshid Amirabadi, Northeastern University, USA
Jongwon Shin, Chung-Ang University, South Korea
Shafiq Odhano, University of Newcastle, UK
Gui-Jia Su, Oak Ridge National Laboratory, USA
Stefano Bifaretti, Univ. of Rome Tor Vergata, Italy
Marco di Benedetto, University Roma Tre, Italy
Mahima Gupta, Portland State University, USA
Jun-ichi Itoh, Nagaoka University of Technology, Japan
Gabriele Rizzoli, University of Bologna, Italy
Mattia Rossi, Politecnico di Milano, Italy

Ludovico Ortombina, University of Padova, Italy
Ralph Kennel, Technical University of Munich, Germany
Dongbo Zhao, Argonne National Laboratory, USA
Ghanshyam Singh Gohil, Hitachi ABB Power Grids, USA
Alessandro Lidozzi, Politecnico di Torino, Italy
Vito Giuseppe Monopoli, Politecnico di Bari, Italy
Carl Ho, University of Manitoba, Canada
Fengxiang Wang, Haixi Institutes, Chinese Academy of Sciences, China
Marcello Pucci, INM-CNR, Italy
Helen Cui, University of Tennessee Knoxville, USA
Fei Lu, Drexel University, USA
Jun Wang, University of Nebraska Lincoln, USA
Arijit Banerjee, University of Illinois Urbana-Champaign, USA
Jungwon Choi, University of Minnesota, USA
Minjie Chen, Princeton University, USA
Alex Hanson, University of Texas at Austin, USA
Emre Gurpinar, Oak Ridge National Laboratory, USA
Abhilash Kamini, Utah State University, USA
Shuo Wang, University of Florida, USA
Hang Dai, GE Research, USA
Hao Tu, North Carolina State University, USA
Han Xiong, GE Research, USA
Omer Gundogmus, GE Research, USA
Meng Huang, Wuhan University, China
Ren Xie, GE Research, USA
Yi Liu, Wuhan University, China
Zhen Tian, Wuhan University, China
Yuhua Du, Temple University, USA
Wei Du, Pacific Northwest National Laboratory, USA
Tianqi Hong, Argonne National Laboratory, USA
Sheng Zheng, Oak Ridge National Laboratory, USA
Li Zhang, Nanyang Technologies University, Singapore
Wenkang Huang, Infineon Technologies, USA
Shuang Xu, North China University of Technology, China
Bo Zhang, Idaho National Laboratory, USA
Wencong Su, The University of Michigan-Dearborn, USA

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Vice Chair: Rukmi Dutta, The University of New South Wales, Sydney, Australia
Vice Chair: Antonio J. Marques Cardoso, CISE | University of Beira Interior, Portugal

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Yang Xu, Ford Motor Company, USA
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Narges Taran, BorgWarner Inc., USA
Nicola Bianchi, University of Padova, Italy
Fan Wu, Rivian Automotive, USA
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Edwin Xikai Sun, Rockwell Automation, China
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Bryan Ruddy, The University of Auckland, New Zealand
Renato Lyra, Aerotech, Inc., USA
David Reigosa, University of Oviedo, Spain
Hassan Eldeeb, BorgWarner Inc., USA
Jose Antonio-Daviu, Universitat Politenica de Valencia, Spain
Antonio Grillo, University of Sheffield, England
Haran Karmaker, TECO Westinghouse, United States
Nick Simpson, University of Bristol, UK
Adam Skorek, University of Quebec at Trois-Rivières, Canada
Rafal Wrobel, Newcastle University, UK
Gerd BRAMERDORFER, Johannes Kepler University Linz, Austria

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SUBRATA SAHA, AISIN AW CO. LTD Japan
Rajesh Deodhar, IMRA Europe S.A.S., France
Athanasios Karlis, Department of Electrical & Computer Engineering, Democritus University of Thrace, Greece
Udochukwu Akuru, Tshwane University of Technology, South Africa
Electrical Drives

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Vice Chair: Pinjia Zhang, Tsinghua University, China
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Yang Xu, Ford Motor Company, USA
Paolo Pescetto, Politecnico di Torino, Italy
Peng Han, Ansys, Inc., USA
Rubino Sandro, Politecnico di Torino, Italy
Roberto Petrella, University of Udine, Italy
David Diaz Reigosa, University of Oviedo, Spain
Giacomo Scelba, University of Catania, Italy
Michele Mengoni, University of Bologna, Italy
Di Pan, GE Research, USA
Zhenbin Zhang, Shandong University, China
Jinia Roy, GE Research, USA
Brij Singh, John Deere, USA

Emerging Technologies and Applications

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Vice Chair: Maryam Saeedifard, Georgia Institute of Technology, USA
Chi-Kwan Lee, The University of Hong Kong, Hong Kong, China
Haoran Wang, Aalborg University, Denmark
Zhen Xin, Hebei University of Technology, China
Harish Sarma Krishnamoorthy, University of Houston, USA
Mahshid Amirabadi, Northeastern University, USA
Óscar Lucia, University of Zaragoza, Spain

Conflict of Interest

Vice Chair: Andrea Cavagnino, Politecnico di Torino, Italy

Power Semiconductor Devices, Passive Components, Packaging, Integration, and Materials

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Vice Chair: Francesco Iannuzzo, Aalborg University, Denmark
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Hongfei Wu, Nanjing University of Aeronautics and Astronautics, China
Mona Ghassemi, Virginia Polytechnic Institute and State University, USA
Hengzhao Yang, ShanghaiTech University, China
Adam Skorek, Université du Québec à Trois-Rivières, Canada
Ruxi Wang, Delta Electronics, USA
Yarui Peng, University of Arkansas, USA
Yuhao Zhang, Virginia Polytechnic Institute and State University, USA
Andrew Lemmon, University of Alabama, USA
Shuo Wang, University of Florida, USA
## Schedule-at-a-Glance

*All session times are in Pacific Daylight Time*

### Sunday, October 10, 2021

#### Meet the Instructors Tutorial

<table>
<thead>
<tr>
<th>Time</th>
<th>Virtual 1</th>
<th>Virtual 2</th>
<th>Virtual 3</th>
<th>Virtual 4</th>
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<tbody>
<tr>
<td>9:00AM-9:45AM</td>
<td><strong>TUTORIAL 9AM1</strong></td>
<td><strong>TUTORIAL 9AM2</strong></td>
<td><strong>TUTORIAL 9AM3</strong></td>
<td><strong>TUTORIAL 9AM4</strong></td>
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<td>9:45AM-10:00AM</td>
<td><strong>BREAK</strong></td>
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<td>10:00AM-10:45AM</td>
<td><strong>TUTORIAL 10AM1</strong></td>
<td><strong>TUTORIAL 10AM2</strong></td>
<td><strong>TUTORIAL 10AM3</strong></td>
<td><strong>TUTORIAL 10AM4</strong></td>
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<td>10:45AM-11:00AM</td>
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<td>11:00AM-11:45AM</td>
<td><strong>TUTORIAL 11AM1</strong></td>
<td><strong>TUTORIAL 11AM2</strong></td>
<td><strong>TUTORIAL 11AM3</strong></td>
<td><strong>TUTORIAL 11AM4</strong></td>
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<tr>
<td>11:45AM-12:00PM</td>
<td><strong>BREAK</strong></td>
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<tr>
<td>12:00PM-12:45PM</td>
<td><strong>TUTORIAL 12PM1</strong></td>
<td><strong>TUTORIAL 12PM2</strong></td>
<td><strong>TUTORIAL 12PM3</strong></td>
<td><strong>TUTORIAL 12PM4</strong></td>
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<td></td>
<td>Applying Artificial Intelligence to Battery State Estimation</td>
<td>Resiliency-Oriented Grid-Interactive Converters: Concepts, Design, and Field Implementation</td>
<td>Cryogenic Power Electronics Design for Electrified Aircraft Propulsion</td>
<td>Hybrid Semiconductor Switches based Power Modules, Converters, and Systems</td>
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<td>12:45PM-1:00PM</td>
<td><strong>BREAK</strong></td>
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<td>1:00PM-1:45PM</td>
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<td><strong>TUTORIAL 1PM2</strong></td>
<td><strong>TUTORIAL 1PM3</strong></td>
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</tbody>
</table>

Nearly 900 Technical Sessions available for flexible viewing to best accommodate your schedule.
## Schedule-at-a-Glance

*All session times are in Pacific Daylight Time*

### Monday, October 11, 2021

<table>
<thead>
<tr>
<th>Time</th>
<th>Virtual 1</th>
<th>Virtual 2</th>
<th>Virtual 3</th>
<th>Virtual 4</th>
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<tbody>
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<td>8:30AM-10:30AM</td>
<td>PLENARY SESSION</td>
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<td>Motor Technology Selection and System Level Optimization Effects in Drive Systems</td>
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<td></td>
<td>Dr. Konstantinos Laskaris</td>
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<td>Principal Motor Designer, Tesla</td>
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<td></td>
<td>Electric Aircraft: From Concept to Reality</td>
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<td>Roei Ganzarski</td>
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<td>CEO, magniX</td>
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<td>Hybrid Electric Systems for the Commercial Aviation Sector</td>
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<td>Christine Andrews</td>
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<td></td>
<td>Hybrid Electric Systems Leader, GE Aviation</td>
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<td>Driving Digital Transformation</td>
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<td>Dr. Irene J. Petrick</td>
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<td>Senior Director of Industrial Innovation in the Internet of Things Group, Intel</td>
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<tr>
<td>10:30AM-12:00PM</td>
<td>EXHIBITS</td>
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<td>Explore more than 30 booths to learn about the latest products to support your projects and make an appointment to meet with a vendor.</td>
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<tr>
<td>12:00AM-12:30PM</td>
<td>BREAK</td>
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<tr>
<td>12:30PM-2:00PM</td>
<td>SS16 Coming Soon: Medium- and High-Voltage Gallium Nitride Power Devices</td>
<td>SS17 Power Electronics-Based Technologies for Grid Stabilization: Grid-Forming Inverters, Control of Inverter-Based Resources (IBRs), and Advanced Testing of IBRs</td>
<td>SS10 Energy Access and Empower Billions of Lives: Technologies, Impact and Opportunities for PELS</td>
<td>SS25 Power Electronics Enabled Power System with High Penetration of Renewables</td>
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<td><strong>PRODUCT SESSION: OPAL-RT</strong>&lt;br&gt;HIL Demo of Solar Inverters</td>
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<td>9:00AM-9:30AM</td>
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<td><strong>PRODUCT SESSION: STMICROELECTRONICS</strong>&lt;br&gt;Evolution of Power Delivery for Cloud Computing</td>
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<td><strong>PRODUCT SESSION: GeneSic</strong>&lt;br&gt;G3R™ SiC MOSFETs, Unparalleled Performance and Robustness</td>
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# Schedule-at-a-Glance

## Wednesday, October 13, 2021

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<td>8:30AM-10:15AM</td>
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<td>Cybersecurity for Power Electronics</td>
<td>&quot;ENSURE&quot; Meets the World—the German Strategic Initiative ENSURE Presents Its Energy Outcomes</td>
<td>Power Electronic Technologies for Distributed Energy Resources</td>
<td>Wide-Bandgap Bidirectional Switches and the Applications They Enable</td>
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### Schedule-at-a-Glance

**Thursday, October 14, 2021**

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<td>9:00AM-10:30AM</td>
<td><strong>SS2</strong> Energy Storage for Grid of the Future: Emerging Technologies, Applications and Trends</td>
<td><strong>SS8</strong> Thermal Design and Control for High Reliability Power Electronics, Electrical Drives, and Batteries</td>
<td><strong>SS13</strong> Standard Development and Industry Engagement Update from IEEE Power Electronics Society</td>
<td><strong>SS23</strong> EMI and Insulation Related Challenges and Solutions for WBG-based Power Electronic Systems</td>
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<td>12:00PM-1:30PM</td>
<td><strong>SS19</strong> Experimental Verification versus Simulation</td>
<td><strong>SS14</strong> Additive Manufacturing for Electric Machines</td>
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**Everything You Need for Power Hardware-in-the-Loop**

Building a quality PHIL setup requires components to be carefully selected not just for their technical capability but also for their inter-compatibility. With the Microgrid PHIL Test Bench, OPAL-RT has taken the guesswork and risk out of PHIL with a turnkey product that offers one of the highest performance and versatile setups in the market.
Motor Technology Selection and System Level Optimization Effects in Drive Systems

Dr. Konstantinos Laskaris  
*Principal Motor Designer, Tesla*

True optimal design constitutes an important competitive advantage in electric vehicles and requires good knowledge of physics modeling, parametric design, numerical simulations, and decision making. Battery, Motor, Inverter and Gear design space is searched during the optimization process. The efficiency and mass of the resulting powertrain affect the total cost, depending on the application. Powertrain compaction is key objective, but it comes with reduction in system efficiency and in many cases, at higher overall cost. Moreover, higher speed, more compact systems, exhibit dominant high frequency losses, introducing the need for advanced modeling to capture the phenomena. This keynote outlines key characteristics of motor, inverter and gears to achieve optimal design with particular focus on the motor technology.

Bio: Dr. Konstantinos Laskaris was born in Athens, Greece, received his diploma in Electrical and Computer Engineering from the National Technological University of Athens (NTUA), his master’s degree in Signal Processing from the Imperial College London, UK, and his PhD in Electric Motor geometry optimization for variable speed drives from the NTUA, Greece. Since 2012, Dr. Laskaris has been the Chief Motor Designer at Tesla Motors, located in the Silicon Valley, California, where he leads the team of motor design engineers. Some of his most significant projects at Tesla include the drive unit system design and optimization for the Model S, Model X, Model 3, Model Y and Semi-Truck vehicles. His research interests include parametric design and loss modeling of synchronous and asynchronous machines using finite element analysis, as well as development of multi-objective optimization methods using supercomputers. Dr. Laskaris has also worked in education, as a laboratory partner in NTUA, teaching the science of electric machines and is a co-founder of the Prometheus team in NTUA, which participates in EV fuel economy contests and holds the Panhellenic fuel consumption record today.
Electric Aircraft: From Concept to Reality
Roei Ganzarski
CEO, magniX

There are multiple companies and programs around the world pursuing electric aviation. From pure battery electric, to Hydrogen electric, to hybrid, these programs are mostly run by new entrants into the aviation industry, with some programs also being introduced by the incumbents. We will review from a new entrants perspective, what it really means to take electric propulsion and aircraft from concept to reality including challenges, pitfalls, and successes. What practical real-world lessons should be considered when taking on such an audacious goal.

Bio: Roei is CEO of magniX, an electric aviation propulsion company. With a vision of connecting communities with low-cost, clean air transportation, magniX is disrupting aviation as we know it. Roei is also executive chairman of Eviation, the electric aircraft OEM bringing a newly designed all-electric aircraft to market. Prior to magniX, Roei was CEO of BoldIQ – a provider of dynamic real-time scheduling optimization software. Under Roei’s leadership, BoldIQ grew from seed software startup to multi-million-dollar profitable SaaS company. Before BoldIQ, Roei was with the Boeing family of companies in continuously increasing roles of responsibility. His last role was Chief Customer Officer for Boeing’s Flight Services division where he led worldwide customer and market facing organizations and was responsible for revenue growth and customer service. Other experiences prior to Boeing include investment banking, corporate finance, advertising, and the military. He is a graduate of Wharton’s Advanced Management Program, earned an MBA from the University of Washington, and a BA in Economics from The University of Haifa. Roei lives with his family in Redmond, Washington, USA.

Hybrid Electric Systems for the Commercial Aviation Sector
Christine Andrews
Hybrid Electric Systems Leader, GE Aviation

GE Aviation is committed to a more sustainable future of flight. One of the enabling technologies the jet engine maker is pursuing to help the aviation industry reach its decarbonization goals is hybrid electric capability for single-aisle aircraft. Single-aisle air transport is the largest civil aviation market, where emissions-reducing technologies would have a major impact. In this presentation, GE Aviation will provide an overview of the challenges and opportunities for making hybrid electric systems a reality for this commercial aviation sector and the integration of motors and generators with gas turbine engines, electrical power distribution systems, electrical power converters, electricity storage and more.
Christine is the Hybrid Electric Systems leader for GE Aviation, responsible for the advancement of all power electronics technology development and integration with the gas turbine. Previously, Christine served as the Business Program Manager for the Aviation business at GE Research, where she evaluated future technologies and developed relevant aviation technologies from supporting next generation platforms to servicing existing fleet to improve revenues.

Christine has been at GE for 8 years, and has held various leadership positions across many disciplines within aviation engineering that have yielded many technology advancements in both the combustor and augmentor modules. She has an excellent track record of establishing and maintaining strong customer relationships by consistent execution of government programs. Prior to joining GE, Christine held various engineering positions at Gulfstream Aerospace.

Monday, October 11  10:00AM-10:30AM

Driving Digital Transformation
Dr. Irene J. Petrick
Senior Director of Industrial Innovation in the Internet of Things Group, Intel

Industry 4.0 harnesses digital tools to make industrial operations smarter, more efficient, and ultimately to operate autonomously. At the heart of this transformation lies data and its ability to drive proactive decisions. To accelerate the digital transformation journey you need both a long term vision and a digital architecture. It’s not just applying digital tools to current operations. Instead, successful companies have had to rethink complex interactions between people, processes, organizational culture and technology. This presentation highlights lessons learned during Intel’s three year study of over 500 people at over 400 companies.

Bio: Dr. Irene J. Petrick joined Intel in 2015 and is Senior Director of Industrial Innovation in the Internet of Things Group. Irene focuses on emerging technology, social, and global trends and their combined impact on the industrial space. Her work highlights the industrial internet of things, edge computing, the transition to intelligent manufacturing and the needs of the future workforce, 3D printing and distributed manufacturing and the new business models that are enabled by intelligent manufacturing. Prior to joining Intel, Irene was a professor at Penn State and has been actively engaged with companies in their innovation and technology strategies for over 25 years, including work with twelve Fortune 100 companies, the U.S. military, and a wide variety of small to medium sized enterprises. Petrick is author or co-author on more than 200 publications and presentations.
Special Sessions

These presentation-only sessions are focused on timely and practical topics in the field.

Wednesday, October 13 8:30AM-10:00AM | 10:00AM-12:00PM

SS1 | Cybersecurity for Power Electronics

SESSION ORGANIZERS:
Prof. Alan Mantooth, Distinguished Professor, The Twenty-First Century Research Leadership Chair in Engineering, University of Arkansas, USA
Prof. Frede Blaabjerg, Danish Professor and Villum Investigator, Aalborg University, Denmark
Prof. Sudip K. Mazumder, Professor, University of Illinois at Chicago

The growing threat of cyber-physical attacks targeting electric power grids is rising in number and sophistication. This special session covers several aspects of trending secure power electronic design, intrusion detections, and resilient control schemes. The concept of cyber resiliency in the realm of power electronics at the system level will be presented. The special session hosts demonstrate the importance of designing power electronics with security features in mind in the context of increasingly connected power electronics as a result of IoT, Industry 4.0, and 5G. Device-level attacks that aim to compromise the controllers, intentional noises and denial of service will be presented considering their impact and potential mitigation approaches. Subsequently, this special session presents the significance of the power electronics at the device-level resiliency as it supports a system-level resilience framework, targeting the new trend of securing the edge following the “resilience by design” principle, and an approach for combining fault-tolerance techniques with cyber-attack surface diversification to make legacy real-time control systems resilient against classes of attacks will also be discussed. More importantly, this session will present several industry-oriented topics (e.g., standardization, HVDC applications, among others), covering the latest development of DER community in the areas of stakeholder education programs, standards development, and cybersecurity research.

SPEAKERS AND PRESENTATIONS:

> Cybersecure Power Electronics – Why You Should Care
  Alan Mantooth, Distinguished Professor, The Twenty-First Century Research Leadership Chair in Engineering, University of Arkansas, USA

> On Resiliency of Power Electronics Against Cyber Attacks
  Frede Blaabjerg, Danish Professor and Villum Investigator, Aalborg University, Denmark

> Control Resilience and Cybersecurity Analytics of Power Electronics Dominated Grids
  Sudip K. Mazumder, Professor
  Mohammad Shadmand, Assistant Professor, University of Illinois Chicago

> Power and Energy Programs at the US National Science Foundation
  Aranya Chakrabortty, NSF Program Manager

> Cyber Attack Resilient High Voltage Direct Current (HVDC) Systems
  Reynaldo Nuqui, Senior Principal Scientist, Hitachi-ABB Power Grids

> DER Cybersecurity Stakeholder Engagement, Standards Development, and EV Charger Penetration Testing
  Jay Johnson, Principal Member of Technical, Sandia National Laboratories

> The Role of Power Electronics in Edge Resiliency
  Salam A Baniahmed, Lead Engineer, Eaton Corporation
SS2 | Energy Storage for Grid of The Future: Emerging Technologies, Applications and Trends

SESSION ORGANIZER:

Dr. Tu Nguyen, Sandia National Laboratories

As the electric grid is rapidly transforming to be more renewable and distributed, there are significant technical issues imposed on the grid due to the highly variable renewable generation (e.g., wind and solar). Furthermore, the increasing threats of natural disasters and cyber/physical attacks have created a significant need to improve the resilience and reliability of current grid infrastructure. Grid energy storage systems (ESSs) can be a flexible grid asset that can help mitigate those issues by providing multiple services to grid operators, utilities, and end-users.

Even though the application space for grid energy storage has grown rapidly over the last few years, there are multiple challenges that need to be addressed in order to further facilitate the incorporation of ESSs in the grid. Therefore, this special session gathers the experts in this field to provide an overview and updates on emerging challenges, recent technologies, applications, and trends in the energy storage area. The information provided in this panel will benefit not only the industry but also other key stakeholders such as policymakers, project developers, and researchers in this area.

SPEAKERS AND PRESENTATIONS:

> **Opening Remark: Energy Storage: Key Component of The Future Grid**
  Imre Gyuk, U.S. Department of Energy, Office of Electricity, Energy Storage Program Manager

> **Energy Storage Standards Development**
  Charlie Vartanian, Senior Technical Advisor, Pacific Northwest National Laboratory

> **Cold Storage - Making Energy Storage Work in the Far North**
  Jim McDowell, Business Development Manager

> **Enabling Energy Storage for Grid Applications Through Advanced Power Electronics**
  Jacob Mueller, Senior Member of Technical Staff, Sandia National Laboratories

> **Long-Duration Energy Storage: Emerging Technologies and Applications**
  Erik Spoerke, Energy Storage Materials Lead, Sandia National Laboratories

SS3 | “ENSURE” Meets the World: the German Strategic Initiative ENSURE Presents Its Energy Cosmos

SESSION ORGANIZERS:

Prof. Dr. Marco Liserre, Christian-Albrechts-Universität zu Kiel (Kiel University)
Maximilian Dauer, Siemens AG

In 2050, the German power generation will mostly rely on renewable sources such as wind and PV and will face a variety of new electrical consumers such as electric vehicles, which requires a restructuring of the entire energy grid. In the German strategic project “New ENergy grid StructURes for the German Energiewende - ENSURE” partners from industry, academia and civil society are developing a holistic solution for the future German energy grid called “Energy cosmos ENSURE” based on new technological concepts, such as sector coupling, innovative power electronics-based assets and profound ICT-integration and automatization, which fit in the actual social-economic framework. Along the way, a digital twin of the entire system is built for co-simulation and advanced testing purposes and selected pilot plants are installed in northern Germany to demonstrate the systematic interaction of the novel
assets, control and protection strategies in the “Energy cosmos ENSURE”. In the first part of the special section the challenges and conceptual solutions of ENSURE will be presented including: Power Electronic Transformer, MVDC grid coupling, dynamically meshed operation of hybrid distribution grids, and Co-Simulation as advanced testing facility for large grid infrastructure projects. The second part is dedicated to international strategic projects to expand the vision of future power grids to a worldwide perspective, including topics such as Hybrid AC/DC networks in the US, new HVDC technologies in China and soft-open points and DC links in the distribution network in UK.

SPEAKERS AND PRESENTATIONS:

> **Unlocking the Hidden Capacity of the Electrical Grid through Power Electronics**
  Marco Liserre, Kiel University

> **High Power Charging System with Power Electronic Transformers**
  Stephan Rupp, Maschinenfabrik Reinhausen

> **Flexibility in Distribution Grids with Medium-Voltage Direct Current (MVDC) - Technology and Use Cases**
  Javier Iglesias, Hitachi ABB Power Grids

> **Evaluation of Dynamically Meshed Network Topologies for Improved Power Flow Allocation and Network Capacity Enhancement**
  Silas Reigardt, SW Kiel Netz GmbH

> **Co-Simulation Test Bench for Grid Integration of Power Electronics Under Usage of Power-Hardware-in-the-Loop**
  Frank Jungnickel, Siemens AG

> **Hybrid HVDC and HVAC Networks with Hybrid Substations for a Carbon-neutral Grid by 2050**
  Johan Enslin, Clemson University

> **Innovation of HVDC Technology to Support the Power Grid to Integrate More Renewable Energy**
  Shukai Xu, China Southern Power Grid

> **Back-to-Back Converters and DC links in UK Distribution Networks**
  Tim Green, Imperial College London; Jun Liang, Cardiff University

**Wednesday, October 13 8:30AM-10:00AM | 10:00AM-12:00PM**

**SS4 | Power Electronic Technologies for Distributed Energy Resources (DERs)**

SESSION ORGANIZERS:

Liuchen Chang, University of New Brunswick
Sonny Yaosuo Xue, Oak Ridge National Laboratory
Hanh-Phuc Le, UC San Diego
Sudip Mazumder, University of Illinois Chicago
Minjie Chen, Princeton University
Yongheng Yang, Zhejiang University
Gab-Su Seo, National Renewable Energy Laboratory
Xiaonan Lu, Temple University
Jin Wang, Ohio State University
Prasad Enjeti, Texas A&M University
Juan Balda, University of Arkansas
Xiongfei Wang, Aalborg University
Ke Ma, Shanghai Jiaotong University
Jose Fernando Jimenez Vargas, Los Andes University
This special session will present a comprehensive overview of the state-of-the-art technologies of power electronics for distributed energy resources (DERs). The topics and their contents include:

**Introduction to Distributed Energy Resources:** DER definitions and values to electric grids, market drivers (including climate change, energy transformation, regulatory policies), world DER market growth in terms of the revenue and capacity, DER grid interconnection requirements and standards, DER grid interoperability requirements and standards, and evolving DER technology trends.

**Integrated Power Components and Subsystems:** Power semiconductor devices: their roles and types including SiC and GaN, photoconductive semiconductor switches based on wideband gap devices, power semiconductor device modules and reliability, passive components, storage technologies, power management, integrated power circuits, power-systems-on-chip, and packaging.

**Power Conversion for DERs:** Overview of the power electronics technologies and converter-level roadmaps for distributed energy resources, including the roles and requirements of power electronics for DER systems, and design and implementation of future DER systems including solar photovoltaic, wind energy, charging infrastructure, and grid-scale energy storage, with focus on power electronics technologies featuring high efficiency, high power density, and advanced functions that are needed to support the future grid.

**Integration and Control of DERs:** Summary of the recent developments in integration and control of DERs, covering the state-of-the-art DER technologies for grid integration and control including power system support functions from DERs, control of individual DERs including grid-forming inverter controls, stability of power systems with inverter-based resources, microgrids and networked microgrids, and protection with a high level of converters for distribution and transmission systems.

**Security, Protection and Resilience for DERs:** Cyber security at the device level and system level, protection (including solid-state protection), reliability, safety, grid system restoration, resiliency-oriented controls, and communication protocols.

**MV and HV Technologies for DERs:** medium-voltage (MV) and high-voltage (HV) power electronic interfaces for distributed energy resources, including the following main topics: energy storage systems (ESS), PV farms and hybrid PV-ESS, wind farms, solid-state transformers, DC networks, and fast chargers for electric vehicles.

**Testing and Validation:** Overview of grid emulation technologies including the hardware-in-the-loop testing methods, along with the challenges and prospects in the end.

**SPEAKERS AND PRESENTATIONS:**

- **Introduction to Distributed Energy Resources, and their Market & Standards**
  - Liuchen Chang, Professor, University of New Brunswick
  - Sonny Yaosuo Xue, Oak Ridge National Laboratory

- **Integrated Power Components and Subsystems**
  - Hanh-Phuc Le, Assistant Professor, UC San Diego
  - Sudip Mazumder, Professor, University of Illinois at Chicago (UIC)

- **Power Conversion for DERs**
  - Minjie Chen, Assistant Professor, Princeton University
  - Yongheng Yang, Professor, Zhejiang University

- **Integration and Control of DERs**
  - Gab-Su Seo, National Renewable Energy Laboratory
  - Xiaonan Lu, Assistant Professor, Temple University

- **Security, Protection and Resilience for DERs**
  - Jin Wang, Professor, Ohio State University
  - Prasad Enjeti, Professor, Texas A&M University

- **MV and HV Technologies for DERs**
  - Juan Balda, Professor, University of Arkansas
Monolithic bidirectional switches hold the potential to trigger a revolution in the future of power electronics technology. Unfortunately, an impressive catalog of high-performance power converter topologies designed to use bidirectional switches has never achieved marketplace success for the lack of commercially available monolithic BD (M-BD) switch devices. No silicon-based M-BD switch technology has emerged that has successfully crossed the threshold into large-scale production. New wide-bandgap power device technology using GaN and SiC has opened intriguing avenues to WBG-based M-BD switches that hold much higher promise for overcoming the barriers to commercialization. Both lateral and vertical M-BD device topologies have been proposed using GaN and SiC that have resulted in prototype devices with ratings as high as 1400V and 100A. The purpose of this special session is to showcase promising state-of-the-art WBG-based M-BD switch technology in a manner that objectively evaluates both their strengths and technical challenges. This session also highlights some of the most promising applications for this prospective new generation of M-BD switches as well as the most likely power converter topologies that will take the fullest advantage of their availability. These application areas cover a wide spectrum extending from ac solid-state circuit breakers to static power converters to motor drives.

SPEAKERS AND PRESENTATIONS:

- **WBG-based Solid-State-Bidirectional-Circuit-Breakers: Device Technology, and Opportunities and Barriers to Mass Market Adoption**
  Victor Veliadis, Chief Executive & CTO, PowerAmerica

- **Monolithic SiC-based Bidirectional FET (BiDfET): Exploring Opportunities & Challenges**
  Subhashish Battacharya, Professor, North Carolina State University
  Dr. B. Jayant Baliga, Professor, North Carolina State University

- **Bidirectional Power Semiconductor Switches: Challenges and Opportunities**
  John Shen, Professor, IIT

- **How Solid-State Circuit Breakers Enable the Energy Transition and How WBG Semiconductors Play a Critical Role**
  Ryan Kennedy, CEO, Atom Power

- **Solid-State Circuit Breaker: Opportunities and Challenges**
  Xiaoqing Song, Sr. Research Scientist, ABB US Corporate Research Center

- **Monolithic Bidirectional Switches - X-Technology of 3-Phase AC/DC Mains Interfaces**
  Johann Kolar, Professor, ETH Zurich

- **The Re-Emergence of Current-Source Inverters in Future Machine Drives Enabled by WBG-Based Bidirectional Switches**
  Thomas M. Jahns, Professor, University of Wisconsin - Madison

- **Power Scaling of Current-Source Inverter Technology with WBG-Based Bidirectional Switches to 100 kW for Electric Vehicle Applications**
  Bulent Sarlioglu, Assoc. Professor, University of Wisconsin - Madison, WEMPEC
Tuesday, October 12 2:00PM-2:30PM

SS6 | Energy Storage Systems: Applications, Control and Interfaces

SESSION ORGANIZER:
Juan Carlos Balda, Professor, University of Arkansas

Power systems are rapidly evolving from few large generating stations into many distributed generating stations ranging from several megawatts to few kilowatts. Higher penetrations of distributed generation are leading to system operating modes not experienced in traditional power systems, for example, intermittent distributed generation and bidirectional power flows. Energy storage systems making use of power electronic interfaces complement intermittent distributed generation and may also provide services like reducing peak demands, particularly in constrained distribution systems, and frequency regulation. Additionally, energy storage is the enabling technology for green transportation, including electric vehicles. Governments are enacting regulations to accelerate the deployment of these systems. This special session will focus on energy storage systems by bringing speakers from different points of view; in particular, government, electric utilities, manufacturers, and academia.

SPEAKERS AND PRESENTATIONS:

> Special Session Outline and Introduction
Juan Carlos Balda, University of Arkansas

> Power Conversion Challenges and Opportunities with Flow Batteries
Paolo Piagi, Lockheed Martin

> Integration of Energy Storage Systems in Solar PV Systems
Francisco D. Freijedo, Huawei Nuremberg

> Design and Evaluation of High Capacity Lithium-ion Battery Energy Storage Systems
Rui Li, Shanghai Jiao Tong University

> Energy Storage Power Electronic Systems Integration Research at ORNL
Michael Starke, Oak Ridge National Laboratory

Tuesday, October 12 2:00PM-2:30PM

SS7 | Advanced Power Electronics Integration for Renewables

SESSION ORGANIZERS:
John Seuss, United States (US) Department of Energy (DOE)
Suman Debnath, Oak Ridge National Laboratory (ORNL)

As hybrid resources (like photovoltaic [PV]) and energy storage systems [ESS]) are integrated into the grid and the interconnection standards are upgraded, it is important to understand newer power electronic topologies that can be utilized to reliably integrate these resources like providing advanced grid services. With the increasing maturity shown by silicon carbide (SiC) devices and their corresponding adoption in certain sectors (like industrial drives), there is a need to explore the next-generation power electronics topologies that can utilize the SiC devices to integrate hybrid resources and provide fast/efficient grid services. In this talk, several ongoing research efforts will be discussed to discuss these newer integration approaches of SiC devices and the corresponding advanced control functionalities that they can introduce.

SPEAKERS AND PRESENTATIONS:

> Overview of Solar Energy Technologies Office (SETO) and Funding Efforts in Power Electronics
John Seuss, United States (US) Department of Energy (DOE)
Temperature has a major effect on both batteries and power electronics (PE) performance and reliability. The development of the electromobility sector and the spreading of PE throughout society (e-mobility, industry, grid, consumer electronics, etc.), together with the trend towards increasing power- and energy- densities, and the rise of PE-based critical applications (more electrical aircraft, grid applications, etc.), therefore puts the spotlight on thermal management and reliability issues. Consequently, limiting peak temperatures, temperature gradients, and thermal cycling of power devices and batteries gains great relevance.

Limiting thermal stress can be achieved by combining high performance cooling technics (jet impingements, phase-change-materials, etc.) and temperature- and reliability- oriented control algorithms (active thermal control, loss reduction strategies etc.). At design stage, achieving high power density call for the use of improved modelling technics for complex and multi-physic systems. Furthermore, lifetime estimation and condition monitoring play an important part in reliability assessment. To reach these targets, high performance temperature measurement, imaging systems, and estimators are required during design and for field monitoring.

The industrial and academic speakers involved in this special session will tackle these issues with both conceptual- and application- oriented presentations. Altogether, they cover a broad area of expertise, including reliability of PE and batteries, thermal-guided design & control strategies, cooling technics, temperature sensing, imaging, and estimation, electromobility, and PE systems.

SPEAKERS AND PRESENTATIONS:

> **Design Challenges of High Power Density Dc-Dc Converters**
  Rik W. De Doncker, Professor, RWTH Aachen University

> **Reliability Driven Control of Power Converter**
  Marco Liserre, Professor, University of Kiel, Kiel
Special Sessions

> **Artificial Neural Network based Thermal Model Considering the Cross-Coupling Effects of Power Modules**
  Frede Blaabjerg, *Professor, Aalborg University*

> **State of the art Thermographic Cameras for Testing Power Electronics and Batteries**
  Stefan Kayser, *InfraTec*

> **Electro-thermal Co-Design of Traction Drive Inverters**
  H. Alan Mantooth, *Professor, Arkansas University*

> **Design of eAxle Propulsion Drives for Electric Vehicles**
  Jin Hwan Jung, *Hyundai Motor Company*

> **Influence of Thermal Gradients on Battery Performance**
  Florian Ringbeck, *RWTH Aachen University*

> **Experimental Thermal Analysis and Reduced Order Modelling of Direct Oil-Cooled PMSM**
  Henning Sauerland, *Hitachi Europe GmbH*

**Wednesday, October 13  2:00PM-3:30PM**

**SS9 | Advances in SiC and GaN Grid Applications to Support EV and Renewable Energy**

**SESSION ORGANIZER:**

**Dr. Victor Veliadis, Executive Director and CTO of PowerAmerica**

The power grid will soon need to accommodate a large EV charging infrastructure while at the same time incorporating more solar and wind energy sources. Panelists will address the following issues specific to medium voltage SiC technology in the context of grid capability:

- MV SiC MOSFET on-resistance, capacitance, and current ratings
- The design/modeling/fabrication of SiC Super junction (SJ) FETs and IGBTs
- Determine optimal SiC device for each grid application domain and device characteristics important to support the grid
- SiC SJ FETs and IGBTs in switching circuits
- Bipolar conduction induced defect generation is impacted by the higher current densities in SJ and IGBT devices

**SPEAKERS AND PRESENTATIONS:**

> **Co-Existence of GaN & SiC: Present and Future**
  Francois Hebert, *GLOBALFOUNDRIES*

> **The High and Low (Power) of SiC and GaN Transistor Applications**
  Tim McDonald, *Infineon Technologies*

> **Advances in Applications of SiC and GaN Technology**
  Rick Eddins, *GE Aviation*

> **Advances in GaN to Support EV and Renewable Energy**
  Larry Spaziani, *GaN Systems*

> **Design of a 6.6 kW Bidirectional On-Board Charger Using GaN with Integrated Driver, Protection and Temperature Reporting**
  Brent McDonald, *Texas Instruments*
Ensuring universal, affordable, and sustainable energy access is one of the biggest societal challenges of our time. As of 2020, close to a billion people worldwide live without having access to electricity, and another two billion have unreliable access. The centralized electric grid is not the optimal choice for remote and rural applications, due to environmental impact, cost, mismatch to user needs and challenges around financial feasibility. Decentralized approaches, such as solar home systems and microgrids, have emerged as a response to shortcomings of the centralized grid approach, but affordability, scalability, interoperability, societal and technical sustainability remain as key challenges. Power electronic technology is one of the key enabling technologies for context-appropriate and sustainable energy access solutions. The IEEE Power Electronics Society (PELS) engaged with Energy Access through organizing a global challenge – Empower a Billion Lives (EBL-I), and by including Energy Access as a new and core topic in its long-range planning. Following on the success of the first EBL-I round, a strong expression of interest in the technologies underlying energy access, and a burgeoning need for decentralized power systems with more autonomous control, the Global Energy Access Forum and the Technical Committee – 12: Energy Access was formed by PELS to provide global technical leadership in this important area.

SPEAKERS AND PRESENTATIONS:

> Segment 1: Welcome and Framing of Technology Opportunities and Issues leading to the formation of the Global Energy Access Forum and Empower a Billion Lives

Deepak Divan, Georgia Institute of Technology, USA – Welcome and Moderator of Segment 1


Three Panelists to address the topics of technology opportunities and issues:

1. Silard Liptak, Agsol, Kenya
2. Shreyas Kulkarni, Endeavour Energy, USA
3. Rajan Kapur, IEEE Smart Village, Larenkelo Ventures, USA

> Segment 2 – Empower a Billion Lives II - Global Competition to Crowdsource Solutions to Energy Access

Jelena Popovic, University of Twente, The Netherlands (moderator) Empower a Billion Lives, PELS TC-12 Energy Access

Three Panelists to address the establishment of PELS TC-12 Energy Access and launching the Empower a Billion Lives -II

1. Issa Batarseh, University of Central Florida, Member PELS TC-12 Energy Access, Empower a Billion Lives, USA
2. Sanjib Kumar Panda, National University of Singapore, Member, PELS TC-12 Energy Access - Chair
3. Deepak Divan, Georgia Institute of Technology, USA, Steering Chair PELS Global Energy Access Forum
### Special Sessions

**Wednesday, October 13 2:00PM-3:30PM**

#### SS11 | Power Electronics Dominated Grids: Dynamic Modeling and Simulation for Reliable and Resilient Operation of Future Grids

**SESSION ORGANIZERS:**

- **Suman Debnath**, Oak Ridge National Laboratory (ORNL)
- **Kemal Celik**, United States (US) Department of Energy (DOE)

With increased penetration of power electronics based resources in the grid like photovoltaic (PV), energy storage systems (ESS), electric vehicle (EV) chargers, among others, there is a need to understand the impact on the reliability and resilient operation of future grids. Towards the same, next-generation tools and control methodologies are required for stable, reliable, and resilient operation of future grids. In this talk, the opportunities in future power grids with increased penetration of power electronics will be presented followed by the presentations on ongoing research activities in development of next-generation tools, models, and control functionalities.

**SPEAKERS AND PRESENTATIONS:**

- **Collaborative Sensor Networks to Assist Data-driven PV Systems Dynamic Modeling**  
  Marissa Enid Morales-Rodriguez, Solar Energy Technology Office (SETO) from the US Department of Energy (DOE)

- **High-Fidelity Electromagnetic Transient (EMT) Modeling of Power Electronics in Future Grid**  
  Suman Debnath, Oak Ridge National Laboratory

- **Holistic Modeling Methods to Improve the Stability of Inverter-Dominated Power Grids**  
  Maozhong Gong, GE Research

- **Dynamic Model Development of Inverter-Based Resources through Experiment Measurements and Real-World Event Data**  
  Lingling Fan, University of South Florida

- **Dynamic Modeling and Fault Simulations of an IPRB Microgrid with Multiple Grid Forming Inverters**  
  Lisa Qi, ABB US Research Center

- **Advanced Generic Dynamic Models for Screening of Reliability and Resilience Challenges of Future Grids**  
  Jens Boemer, Electric Power Research Institute (EPRI)

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**Thursday, October 14 10:30AM-12:00PM**

#### SS12 | Advanced Design and Manufacturing Techniques for Electric Machines – Simulation and Test

**SESSION ORGANIZER:**

- **Prof. David Lowther**, Professor, McGill University

The session is intended to provide a series of presentations highlighting the industrial needs and processes for electrical machine design and manufacture and recent university research which is targeted at addressing these needs. The topics covered will range from the design process, as illustrated by the V-cycle approach, and including the multi-physics performance of a proposed motor-drive system; the use of additive manufacturing technologies to enable the implementation of machine topologies which cannot be constructed using conventional, mostly subtractive, manufacturing techniques; and the use of state of the art machine learning techniques to enhance and assist in the design of novel electrical machine architectures. Finally, while “digital twin” technology is driving simulation to new levels and, hopefully, reducing the number of prototypes needed, the physical implementation must be tested to verify the performance of the real device and the issues involved in fast and accurate testing will be discussed.
SPEAKERS AND PRESENTATIONS:

> **Fundamental Design Process of Common Electric Machines**
  James Hendershot, *MotorSolver LLC*

> **Multi-Physical Simulations in Electric Machine Design - Challenges and Opportunities**
  Tanvir Rahman, *Siemens Digital Industries Software*

> **Machine Learning Techniques for the Topology Optimization of Electrical Machines**
  David Lowther, *McGill University*

> **Additive Manufacturing of Complex Shape Hard Magnetic Materials for PM Machines**
  Pragasen Pillay, *Concordia University*

> **Fast, Accurate and Comprehensive Testing of Traction Machines**
  Narayan Kar, *University of Windsor*

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**Thursday, October 14**  
9:00AM-10:30AM

**SS13 | Standard Development and Industry Engagement Update from IEEE Power Electronics Society**

**SESSION ORGANIZERS:**

- **Johan Enslin**, Chair of PELSC, Clemson University
- **Matt Wilkowski**, Vice chair of PELSC, EnaChip
- **Xu She**, Secretary of PELCS, Carrier Corporation

IEEE power electronics society (PELS) is the driving force behind numerous standard activities and international technology roadmap initiatives relating to power electronics. With the motivation of providing our community the latest update of the standard development within IEEE PELS as well as promoting better involvement especially from industry, this special session brings together the leading experts who are actively leading the development efforts of power electronics standards. The topics span from component (capacitor, magnetics, etc.) level development to system level integration (grid forming inverter, etc.).

**SPEAKERS AND PRESENTATIONS:**

> **Activities of the IEEE PELS Standards Committee and Industry Engagement**
  Johan Enslin, *Professor of Clemson University and PELS VP of Standards*

> **Updating ETTC Standards for Magnetic Components to Keep Pace with Technology**
  George Slam, Senior Applications and Content Engineer Wurth Elektronik, Chairperson ETTC

> **Keeping IEC TC 51 Standards Relevant to Market Applications of Magnetics**
  Mark Swihart, Vice President of Technology and Product Development, Magnetics division of Spang and Company

> **Impact of Near Magnetic Field Characteristics on Performance of Converters**
  Matt Wilkowski, Vice President of Magnetics Technology and Engineering, EnaChip
  Fang Luo, Director of Spellman High Voltage Electronics Laboratory, Stony Brook University

> **Development of IEEE 2988: Recommended Practice of Use and Functions of Virtual Synchronous Machines**
  Qing-chang Zhong, Professor, Illinois Institute of Technology

> **Update on Grid-forming Inverters Working Group and Standard Development**
  Leo Casey, Google X
**Thursday, October 14**

**SS14 | Additive Manufacturing for Electric Machines**

**SESSION ORGANIZERS:**
- Franco Leonardi, Ford Motor Company
- Leyi Zhu, Ford Motor Company

The special session will focus on what is currently available in terms of additive manufacturing solutions for electric machines, and also on what is coming in the near and far future.

**SPEAKERS AND PRESENTATIONS:**
- Arian Aghababaie, PhD, Holo Inc.
- **Additive Manufacturing of Copper Hairpin Windings**
  Jakob Jung, Additive Drives GmbH
- **Design Concepts for Electric Machines leveraging Additive Manufacturing**
  Jagadeesh Tangudu, Raytheon Technologies Research Center
- **Additive Manufacturing of High Performance Stator and Rotor Structures for Electric Machines**
  M. Parans Paranthaman, Oak Ridge National Lab
- **3D Printed Permanent Magnets: From Material Fundaments to e-Motor Applications**
  Jaćim Jaćimović, ABB Switzerland
- **Rapid Development of Electric Machines - Casting with AM**
  Keith Denholm, Grainger & Worrall

**Wednesday, October 13**

**SS15 | Future of Wide-Bandgap Devices SiC, GaN and Diamond and Their Emerging Applications in Power Electronics**

**SESSION ORGANIZER:**
- Dr. Tanya Gachovska, Solantro Semiconductor Corp.

The need for high-voltage devices operating at high frequencies and temperature is growing, especially for advanced power electronics. Si-based power devices has some limitations. They are not able to meet these requirements without connecting many devices in series and in parallel, using snubbers and expensive cooling systems. Thus, the market of Si power devices have started to decrease and wide band gap semiconductors have attracted considerable attention. Research into SiC, GaN, and diamond as materials for power devices has been carried out over the past two decades.

SiC, GaN and diamond have wide-bandgap energy resulting in higher breakdown strength for a given blocking voltage. Therefore, SiC, GaN and diamond devices have smaller drift layers or channel lengths as compared to Si devices. As a result, the storage of the minority carriers or the input and output capacitance and, therefore, the switching losses are reduced. This leads to an increase of the switching frequency high than 0.5 MHz, reducing power systems passive components size and cost. In the last 5 years, more companies manufacturing SiC and GaN have come to the market and new startup companies have arised. The prace of SiC and GaN devices have decrease draticaly and more application.

This special session will offer insights into the future and trends for SiC, GaN and diamond devices and their emerging applications in power electronics.
Special Sessions

SPEAKERS AND PRESENTATIONS:

> **The Future of SiC Devices and Their Emerging Applications in Power Electronics**  
  Anuj Narain, *Director Power Platforms and Applications at Wolfspeed*

> **The Future of SiC and GaN Devices and Their Emerging Applications in Power Electronics**  
  Filippo Di Giovanni, *WBG Strategic Marketing Manager within the Power Transistor MACRO Division at ST Microelectronics, based in Catania, Italy*

> **The Future of Diamond Devices their Emerging Applications in Power Electronics**  
  Julian Styles, *Director Business Development at GaN Systems LLC*  

**Monday, October 11 12:30PM-2:00PM**

**SS16 | Medium- and High-Voltage Gallium Nitride Power Devices**

SESSION ORGANIZERS:

*Yuhao Zhang*, Assistant Professor, Virginia Polytechnic Institute and State University  
*Dong Dong*, Assistant Professor, Virginia Polytechnic Institute and State University

After two decades of relentless development, GaN power high-electron-mobility transists (HEMTs) have been commercialized in voltage classes up to 650 V. In the last few years, strong momentum has emerged in industry and academia to extend the application space of GaN devices into the medium-voltage applications. Several industrial companies are developing 650-1200 V lateral GaN HEMTs, either discrete devices or modules. For example, VisIC is developing 800-1200 V GaN HEMT modules for EV applications. Meanwhile, 600-1200 V GaN transistors based on the vertical architecture are being manufactured on 100-mm GaN-on-GaN platform by several companies in the U.S. and Japan. For example, NexGen Power Systems has demonstrated the production of 650-1200 V vertical GaN JFETs with avalanche capabilities; Toyoda Gosei has demonstrated 1200 V vertical GaN MOSFETs with over 100 A current. In academia, high-voltage GaN devices have been demonstrated up to 10 kV recently. All these progresses suggest that the industrial medium-voltage GaN devices will be available to power electronics engineers very soon. This special session provides a timely overview of the state-of-the-art medium- and high-voltage GaN devices and an industry-centric discussion on the benefits and applications spaces of these coming devices.

SPEAKERS AND PRESENTATIONS:

> **Unlocking the Full Potential of GaN in Power Systems**  
  Dinesh Ramanathan, *Founder and Co-CEO, NexGen Power Systems*

> **Development Status of Vertical GaN SBDs and MOSFETs**  
  Tohru Oka, *Project General Manager & Senior Principal Engineer, Toyoda Gosei*

> **Vertical Gallium Nitride Devices for Medium-Voltage Power Electronics**  
  Robert Kaplar, *Manager of the Semiconductor Material and Device Sciences Department, Sandia National Laboratories.*

> **650V Lateral GaN Devices and Modules for 400V and 800V EV Applications**  
  Tamara Baksht, *CEO, VisIC Technologies*

> **Healthcare Applications of GaN Power Devices**  
  Juan Sabate, *Senior Principal Engineer, GE Global Research Center*
**SS17 | Power Electronics-Based Technologies for Grid Stabilization: Grid-Forming Inverters, Control of Inverter-Based Resources (IBRs), and Advanced Testing of IBRs**

**SESSION ORGANIZERS:**

**Jing Wang,** *National Renewable Energy Laboratory*

**Andy Hoke,** *National Renewable Energy Laboratory*

Recently, power inverters with grid-forming capabilities have attracted broad interest because these inverters do not rely on external voltage sources to generate power, which pertains better stability and reliability than traditional grid-following DERs. Thus, research and development work on grid-forming inverter has extensively carried out in academia and industry. In this panel, we will first introduce the grid-forming inverter manufacture, ABB, to present their latest development in grid-forming battery inverters. Then, one researcher from Siemens will present their research work on performance comparison of different grid-forming control strategies. Apart from the grid-forming inverter related topic, power electronics control and hardware-in-the-loop is also covered. A senior researcher from NREL will demonstrate the operation of a transmission system (Maui in Hawaii) at and near 100% IBRs.

The professor from University of Alabama will share his latest research work on investigating many abnormal operations of IBRs and IBR plants and present an eye-opening discovery of the shortcomings of traditional dq control. In the end, the research engineer from NREL will present the advanced testing and validation of grid-connected inverters through power-hardware-in-the-loop simulation from small scale (single inverter) to large scale (100+) inverters. This panel will include power electronics-based technologies for grid stabilization, which will be informative, educational and insightful to learn the state-of-the-art and future directions of power electronics-based resources and systems.

**SPEAKERS AND PRESENTATIONS:**

- **Development of Grid-forming Inverters for Supporting Robust and Reliable Future Grids**
  John Glassmire, *Hitachi-ABB power Grids*

- **Performance Comparison of Parallel Grid-Forming Inverters**
  Amit Pandey, *Siemens*

- **Operation of a Transmission Electric Power System At and Near 100% Inverter-based Resources**
  Andy Hoke, *NREL*

- **Impacts of Equivalent Dynamic Grid Impedance and Dynamic Inverter P-Q Capability on Reliability and Harmonic Stability of Inverter Based Resources Plant**
  Shuhui Li, *University of Alabama*

- **Advanced Power-Hardware-in-the-Loop Evaluation of Inverter-based Resources (IBRs)**
  Jing Wang, *NREL*
In aircraft electrification, increasing the specific power and efficiency of electrical components is critical to make more electric/hybrid/electric propulsion physically and economically feasible. Additive manufacturing enables unique geometries that conventional manufacturing techniques cannot achieve. Additively-manufactured applications in electric machines and power electronics include permanent magnets, shafts, and housing designs, thermal management systems, integrated motor drives, additively manufactured coils, and highly optimized traditional designs. Additive manufacturing techniques also allow for increased design flexibility and co-design of related components and subcomponents, allowing for greater system-level performance optimization.

SPEAKERS AND PRESENTATIONS:

> **Exploring Potential of Additive Manufacturing for Next Generation Electrical Machines and Power Electronic systems**
  Peter de Bock, ARPA-E, US Department of Energy

> **Additive Manufacturing of Fe-Si Soft-Magnetic Alloys**
  Alex Plotkowski, Oak Ridge National Laboratory

> **3D Printed Magnetic Cores for Electrical Machines**
  Shanelle N. Foster, Assistant Professor, Department of Electrical and Computer Engineering, Michigan State University

> **Developing the Supply Chain for the Next Generation High-speed, High Voltage Synchronous Reluctance Motor**
  Ozge Taskin, Ricardo

> **Additive Manufacturing of Next Generation Electrical Machine Windings**
  Nick Simpson, Senior Lecturer, University of Bristol

> **Design of a Highly Saturated High-frequency Electric Machine with Additively Manufactured Coils**
  Max Liben, Chief Technology Officer, H3X Technologies Inc.
A fundamental issue that is inherent in engineering work regards how we can verify our theoretical calculations. For some applications, prototyping is relatively convenient, and the experimental results are preferred as proof that the theory is correct, but in the motor and drives world, while some prototyping is possible—most of those cases involve smaller machines and lower power drives as well as less demanding measurement accuracy; but electric machines and drives testing generally become more expensive and time consuming as the machine and drive rating increases, which require massive resources in terms of hardware, software, and manufacturing expertise to conduct valid experiments with confidence. In this era of powerful computational capabilities, many simulations tools have developed to the point where the accuracy of the simulation results have grown and the time it takes to do these simulation calculations have minimized over the years. Complex analysis of interconnected systems can also be done in a timelier manner, and the development of HIL has made the simulation environment more flexible and more realistic. Ultimately, the question is whether the simulation results are trustworthy enough to be accepted as the proof of the proposed technical solution and design? Has the simulation software attained a level of accuracy which matches and/or surpasses the accuracy of experimental results?

**Panelists:**
- Ayman EL-Refaie, Professor, Marquette University
- Andy Knight, Professor, University of Calgary
- Mike Degner, Ford
- Joel Van Sicke, MathWorks
- Sudipta Chakraborty, Director – Energy Systems, OpalRT
- Takeshi Yamada, JMAG
- Philippe Wendling, Altair

**First session topics**
- Discussion on testing accuracy (measurement, stackup, numerical specifications).
- Discussion on simulation accuracy (modelling errors, computation errors, stackup).
- How to determine testing accuracy.
- How to determine simulation accuracy.
- Calibration issues
- Uncertainties.

**Second session topics**
- Open discussion on open questions:
  - At what point can simulation results be considered adequate to testing during the intermediate steps of the R&D cycle? If ever?
  - At what point can simulations results be considered adequate to replace testing as the final proof of verification prior to manufacture, if ever?
  - At which stage during the R&D cycle should simulation results be adequate to push the R&D timeline forward?
  - How to determine the optimal way of employing simulation and experimentation in a product R & D cycle? In a research cycle?
  - Reviewer discretion: when should reviewers insist on experimental results?
Special Sessions

Tuesday, October 12 2:00PM-2:30PM

SS20 | PV Inverter Reliability: Industry Status, Technical Gap, and Future Needs

SESSION ORGANIZERS:
Zheyu Zhang, Assistant Professor, Clemson University
Ramanathan Thiagarajan, National Renewable Energy Laboratory

Motivated by the fact that field data from PV power plant operators show that power electronics converter contributes most to operation and maintenance (O&M) events, responsible for between 43% and 70% of the service calls, it is essential to initialize a conversation focusing on the PV inverter reliability with a wide range of stakeholders’ participation. This could benefit the research community to better understand the industry status, technical gap, and future needs. In this panel, the audience will hear diverse opinions shared by five distinguished panelists from PV power plant operators and PV inverter manufacturers to U.S. DOE Solar Energy Technologies Office and National Renewable Energy Laboratory, and their visions to improve PV inverter reliability in the field. This panel will include the effects of PV inverter reliability on the overall energy yield of PV Power plants from a PV plant operator. This will be followed by presentations on the effects of PV inverter design on inverter reliability by two leading PV inverter manufacturers. Following this, speakers from DOE and NREL will describe the past and current efforts on inverter reliability and standards development work performed within the DOE space.

SPEAKERS AND PRESENTATIONS:

> Importance of Inverter Reliability on Operating PV Power Plants
   Nick de Vries, SVP of Technology and Asset Management, Silicon Ranch

> Solar Inverter Reliability: DOE-funded R&D and Future Directions
   Tassos Golnas, Technology Manager, U.S. DOE Solar Energy Technologies Office

> Insights into Reliable PV Inverter
   Daniel Clemens, Reliability Technical Manager, SMA Solar Technology

> Design of Reliability for PV Inverter
   Matt Ursino, Senior Reliability Engineering, YASKAWA Solectria

> Overview of IEC 62093 ed 2- The PV Inverter Qualification Test
   Peter Hacke, Senior Scientist, National Renewable Energy Laboratory

Thursday, October 14 10:30AM-12:00PM

SS21 | P2964 IEEE Standard for Datasheet Parameters and Tests for Integrated Gate Drivers

SESSION ORGANIZER:
Dr. Tanya Gachovska, Solantro Semiconductor Corp.

Power electronics applications employ power switches. Every switch requires a gate driver, a power amplifier that receives a low-power input from a controller and produces a high-current driving output for the gate of high-power switches such as an IGBT, MOSFET, JFET, or HEMT. Some gate drivers have protection features such as fast short-circuit protection (e.g. DESAT), active Miller clamp, shoot-through protection, shutdown, and overcurrent protection, which make them well-suited for both silicon and wide-bandgap power devices.

However, it is difficult to compare the IC gate drivers using their datasheet parameters. Every company names the drivers’ pins and parameters with different approaches. The parameters are tested at different conditions and methodology or in most of the cases the conditions and the methodology are not given. Only some companies give the selected test circuits. IEEE-IAS-PEDCC has initiated a working group for a standard to provide datasheet parameters and tests for integrated gate drivers, which include
non-isolated gate drive, level-shifted gate drive, and isolated gate drive. The standard scope includes terminology, mnemonic, and pins’ description; parameters and definitions; and test methods and conditions to obtain the parameters. The special session will include an update on the working group.

SPEAKERS AND PRESENTATIONS:

> **Standardization of “Active Miller Clamp”**
  Wolfgang Frank, *Infineon Technologies in Munich/Germany*

> **Gate Driver Absolute Maximum Ratings**
  Zheyu Zhang, Assistant Professor, *Clemson University*

> **Why Do We Need a Standard for Gate**
  Cong Li, *General Electric*

> **Desaturation Protection Requirement**
  Inki Park, ON Semiconductor

> **Comparing Gate Driver Timing and Drive Strength Specifications**
  Ryan Schnell, *Analog Devices iCoupler*

> **Gate Driver Pin Naming Standard**
  Srivatsa Raghunath

**Wednesday, October 13**

| SS22 | Grid Integration of Inverter-Based Distributed Energy Resources: Operation, Planning, and Guidelines |

**SESSION ORGANIZERS:**

Dr. Jianzhe Liu, Argonne National Laboratory (ANL)

Dr. Kun Zhu, Midcontinent Independent System Operator (MISO)

Dr. Jens C. Boemer, Electric Power Research Institute (EPRI)

Dr. Reza Ghaemi, General Electric Research (GER)

Dr. Xuan Wu, American Electric Power (AEP)

The US power system will be undertaking an monumental transformation as it moves forward to being 100% clean energy powered by 2035. In the meantime, the extreme weather events and increasingly stressed loading conditions have put grid stability, reliability, and resilience at risk. The growing power electronics intensive inverter-based resources (IBRs) have a promising potential in contributing to the clean energy transformation while improving system performance. Challenges for IBRs grid integration include: 1) how to design stability guaranteed control for the power electronics interfaced resources that usually have low inertia and high stochasticity; 2) the grid planning issue of IBR could be significantly different from the conventional ones; 3) what the industrial guidelines for IBRs integration would be in the future as they have already undertaken significant changes; 4) how we should control a large-scale fleet of IBRs as the problem is high-dimensional and computationally challenging; and 5) how we should optimize the location of energy storage systems given their critical role in IBR’s grid integration and considering the numerous choice of location and capacity. This special session will provide useful industry insights into all the aforementioned issues.

SPEAKERS AND PRESENTATIONS:

> **Robust Optimal Power Flow with Stability and Feasibility Guarantees for Systems with Large-Scale IBR Integration**
  Jianzhe Liu, ANL

> **NERC SPIDER Working Group Update**
  Kun Zhu, MISO

> **State of The Art and Potential Improvements to Existing IBR Distribution Interconnection Practices & Studies**
  Jens C. Boemer, EPRI
Special Sessions

- Scalable Demand Side Control for High Renewable Penetration Distribution Grid: Solutions and Challenges
  Reza Ghaemi, GER

- Optimal Planning & Operations of Batteries in Distribution Systems to Enhance Reliability and Attain Market Revenues
  Xuan Wu, AEP

Thursday, October 14 9:00AM-10:30AM

SS23 | EMI and Insulation Related Challenges and Solutions for WBG-based Power Electronic Systems

SESSION ORGANIZERS:
Bulent Sarlioglu, Associate Professor, University of Wisconsin
Jin Wang, Professor, Ohio State University

Wide bandgap device-based power converters are expected to bring significant efficiency and power density improvements for a wide range of applications. Through the effort from both industry and academia, many circuit implementation and system-level related challenges for implementing WBG devices have been solved. But EMI and insulation degradation are still two significant concerns because of the high dv/dt and di/dt switching of WBG devices. Thus, the organizers of this proposed special session has invited speakers from NASA, industry and academia to present details of EMI and insulation related challenges and solutions.

SPEAKERS AND PRESENTATIONS:

- Reliability as a Barrier for More Electrification
  Isik C. Kizilyalli, the Associate Director for Technology at the Advanced Research Projects Agency - Energy (ARPA-E)

- Challenges of Megawatt Scale Electric Aircraft Propulsion
  Andrew Woodworth, Hybrid Electric Aircraft Materials Technical Lead, NASA Glenn Research Center

- Presentation Title: On Some Problems and Solutions Related to High Voltage in Aviation
  Thierry Lebey, High Voltage Team Leader, Safran

- Minimizing EMI Suppression Penalties in Motor Drive Applications using Wide-Bandgap Current Source Inverters
  Dr. Thomas M. Jahns, Professor, University of Wisconsin - Madison, WEMPEC

- EMI Noise Signatures of Si- vs. WBG-based Traction Inverters
  Chingchi Chen, Technical Leader, Ford Motor Company

Tuesday, October 12 2:00PM-2:30PM

SS24 | Booming the Blue Economy: A New Era for Wave and Hydrokinetic Energy

SESSION ORGANIZERS:
Dr. Yue Cao, Assistant Professor, Oregon State University
Dr. Jing Sun, Michael G. Parsons Collegiate Professor, Univ. of Michigan - Ann Arbor
Dr. Ted Brekken, Professor, Oregon State University; Co-Director, Pacific Marine Energy Center (PMEC)

The Pacific Northwest (including Oregon, Washington, Alaska, and British Columbia-Vancouver area), among other global coastal regions, has access to abundant renewable energy embedded in ocean and river. The energy, known in either a wave form (vertical motion) or a current form (horizontal motion), still
has limited power generation capacity to local grids due to several challenges, especially at the core of mechanical to electrical energy conversion. In 2019, the US Department of Energy (DOE) launched the Powering the Blue Economy initiative to explore the billions of dollars worth of such hydro energy potential alongside the coastline and riverine communities. This decade marks a new era for research and development to bring the highest efficiency, highest reliability, and lowest cost for such blue energy usage, enabling a wide range of adoption. In this session, four industry and one government panelists and three academia organizers will present and discuss the technology needs, innovations, and demonstrations of converter-level design, interdisciplinary control co-design, and system integration to the microgrid/grid connection. The panel organizers and panelists especially represent recently funded projects by the DOE ARPA-E SHARKS program and several DOE Water Power Technology Office initiatives.

SPEAKERS AND PRESENTATIONS:

> **Establishing the ARPA-E SHARKS Program**  
Mario Garcia-Sanz, Program Director, DOE ARPA-E

> **Sustainable, Predictable Renewable Hydrokinetic Energy for Off-Grid Communities**  
Alexandre Paris, President & CEO, ORPC Canada

> **Irony & Urgency: Environmental Review of Wave and Hydrokinetic Energy Projects**  
Paul T. Jacobson, Principal Technical Leader, Water and Ecosystems, EPRI

> **Direct Drive Wave Energy & Pulsed Power Conversion**  
Joseph H. Prudell, Director Oregon Corporate Operations, C·Power

> **Impact of Control Methodology on MHK Device Design**  
Alan McCall, Centipod Program Director, Ecomerit Technologies

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**Monday, October 11 12:30PM-2:00PM**

**SS25 | Power Electronics Enabled Power System with High Penetration of Renewables**

SESSION ORGANIZERS:

**Dr. Rui Yang**, Senior Research Engineer, National Renewable Energy Laboratory (NREL)

**Dr. Mahshid Amirabadi**, Assistant Professor, Northeastern University

The recent advancements in power electronics, solid-state technologies and artificial intelligence are reshaping the traditional view of power distribution systems. The number of power inverters and converters increases significantly due to the high penetration of renewable energy sources and energy storage devices. While much progress has been made in advancing power grids and power electronics technologies, less attention has been paid to bridging the gap between these two traditionally disjoint areas. This session will cover a broad range of contents in the area of power electronics enabled power systems, to address topics such as the impacts of high penetration of renewables and power converters to the power grid and electricity market.

SPEAKERS AND PRESENTATIONS:

> **Transportation Electrification and Its Grid Impacts**  
Xi (Lucy) Lu, Ford Motor Company

> **Impacts of High Penetration Renewables on Voltage and Frequency Stability**  
Yizhe Xu, Grid-Bridge

> **The Application of Siemens SCUC Engine On Various Electricity Markets**  
Ting Dai, Siemens

> **On Board Charger: Transforming EV/HEV into Distributed Energy Resources**  
Zhe Zhang, Eaton
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Advantages of our MasterGaN platform embedding a half-bridge driver based on silicon technology along with a pair of gallium-nitride (GaN) transistor

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- **More robust**: offline driver optimized for GaN HEMT for fast, effective and safe driving and layout simplification

**Board area and weight** are becoming limiting factors as power demands increase. Reducing size and weight can **cut the total cost of ownership** by making installation and maintenance both easier and quicker.

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<th>Part number</th>
<th>General description</th>
<th>Supply voltage max (V)</th>
<th>Key features</th>
<th>Output Current max (A)</th>
<th>High side $R_{DS(on)}$ (mΩ)</th>
<th>Low Side $R_{DS(on)}$ (mΩ)</th>
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<td>MASTERGAN1</td>
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<td>Undervoltage lockout, Interlocking function, Over-temperature, Bootstrap diode</td>
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<td>High power density 600V half-bridge high voltage driver with two 650V enhancement mode GaN HEMT</td>
<td>11</td>
<td>Undervoltage lockout, Interlocking function, Over-temperature, Bootstrap diode</td>
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<td>High power density 600V half-bridge high voltage driver with two 650V enhancement mode GaN HEMT</td>
<td>11</td>
<td>Undervoltage lockout, Interlocking function, Over-temperature, Bootstrap diode</td>
<td>6.5</td>
<td>225</td>
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Photovoltaic Systems – From Basics to Advanced Grid Supportive Control

INSTRUCTORS: Yongheng Yang
Zhejiang University, China

Photovoltaic (PV) is one of the renewable favorites with a fast-growing rate, and much more are expected in the future. However, the massive installation of grid-connected PV systems creates many challenges to the grid. Many attempts have thus been made and being explored to better integrate PV systems as grid-friendly systems, which not only minimize the impact on the grid but also offer smart controllability and flexible manageability to enhance the grid performance. To master and then further advance the PV technology, we should understand the basics of PV systems like how the power conversion works, what the technological bottlenecks are, and what is the potential impact for largescale adoption of power electronic-based PV systems? And, how do we address the increasing issues outlined, e.g., by grid codes and standards, IEEE Std. 1547-2018, through advanced control strategies? The tutorial is proposed, and it is dedicated to tackling the technological challenges of nondeterministic power generation in grid-connected PV systems. It provides a step-by-step design of grid-friendly PV systems including the PV modules and power converters, and then a comprehensive understanding of the basics of PV systems. The focus is to innovate and improve the operability by means of advanced control to create more sustainable, grid-friendly, and reliable PV systems that comply with grid regulations and contribute to reducing the cost of energy. This enables the grid-supportive operation of PV systems, e.g., inertia emulation, to improve the grid performance. The tutorial is intended for intermediate and advanced audiences in the field of power electronics, engineers, and researchers, who are looking for advanced control solutions to power converters, especially focused on PV conversion systems for distributed generation. Researchers and engineers who seek for the basic knowledge for the PV technology are welcomed.

Pulse-Width-Modulation: with Freedom to Optimize EMI

INSTRUCTORS: Dong Jiang¹, Zicheng Liu¹, Qiao Li²
¹Huazhong University of Science and Technology, China; ²Hunan University, China

This tutorial focuses on studying of electromagnetic interference (EMI) reduction oriented advanced pulse-widthmodulation (PWM) technologies. EMI is a serious threat to the reliability and operation of the power electronics converters as motor drives. PWM is one of the most important factor for EMI in power electronics converters. Advanced PWM methods can be developed with freedom to optimize EMI and other parameters. In this tutorial, series work of advanced PWM which used freedom to optimize performance will be presented. The tutorial begins with the introduction of EMI problems and PWM technologies in power electronics converters. Then, PWM’s impact on the converter performance is discussed for power losses, current ripple and EMI. With current ripple as the control target, the prediction model is studied as the basis for PWM. On the basis of the prediction model, variable switching frequency PWM (VSFPWM) is proposed. This method can improve the EMI and power losses together with control of current ripple or other related parameters. For the general issues of VSFPWM, including the impact on harmonics and feedback control will also be illustrated. For common-mode (CM) EMI issue, this tutorial is with special care. PWM can help to reduce the CM voltage of regular two-level converter, but cannot eliminate it theoretically. For three-level converter, zero CM PWM is possible but with many penalties. A novel zero-CM PWM method for paralleled converters is introduced, together with its further improving methods. This method can be integrated with electric machine and further improve the power density. This tutorial is based on the series work of the presenters’ group as well as researchers all over the world. The major contents are included in the newly published book “Advanced Pulse-Width-Modulation: with Freedom to Optimize Power Electronics Converters” by Springer Press.
9AM3 | A MATLAB/Simulink Approach of Photovoltaic Power Systems: Designing, Modeling, Simulation, and Control

INSTRUCTORS: Weidong Xiao¹, Jimmy Chih-Hsien Peng², Qiang Han³
¹University of Sydney, Australia; ²National University of Singapore; ³BC Hydro, Canada

This tutorial provides a practical introduction to photovoltaic (PV) power systems regarding the theoretical analysis, design, modelling, control, and simulation. The objective is to expose the audience to all facets of PV power systems with emphasis on the hands-on tools required for executing academic research and for meeting industry expectations. The development of this tutorial is based on the diverse experience and expertise of the presentation team in both the academia and power industry. The tutorial will first begin with the fundamentals of PV systems regarding theoretical analysis and design. The modelling includes two parts; one for simulation, and another for dynamic analysis. Based on the system dynamics, a control design approach for grid-forming inverters is introduced to guarantee the system stability and robustness in the presence of multiple PV systems. Finally, transient studies for transmission-level connected PV systems are presented. All analysis and simulation are conducted using function blocks in MATLAB\Simulink environment. After the tutorial, the audience shall be able to design a practical grid-tied PV power system, simulate its operation, and evaluate its performance via MATLAB\Simulink. The tutorial will be organized to facilitate smooth transitions from the fundamental and practical knowledge to more advanced subjects.

10AM1 | Emerging Bidirectional Switches and Their Impact on Future AC Power Converters and Applications

INSTRUCTORS: Thomas M. Jahns¹, Bülent Sarlioglu¹, Johann W. Kolar², Jonas Huber², Victor Veliadis³
¹University of Wisconsin – Madison, United States; ²ETH Zurich, Switzerland; ³PowerAmerica, United States

This tutorial will introduce participants to the emerging technology of monolithic bidirectional (M-BD) power switches and the exciting opportunities they will open for future ac power converter topologies and their applications. Wide-bandgap (WBG) power semiconductor technology has opened the door to the development of long-sought monolithic bidirectional switches that can block voltage and conduct current in both polarities under full gate control in all four quadrants. Prototype versions of these M-BD devices produced in industrial and academic laboratories have been reported with ratings as high as ±1400V and ±100A. A section of this tutorial will be devoted to exposing participants to the underlying semiconductor technology of these M-BD switches including their operating principles, achievable terminal characteristics, technical challenges, and promising M-BD devices reported to date. Following this introduction to state-of-the-art M-BD switch technology, the tutorial will focus attention on the strong potential this new class of switches holds for having a major disruptive impact on the future of power electronics. In particular, this tutorial will focus on the exciting opportunities that these new switches provide for dramatically improving key performance metrics of future dc-ac and ac-ac power converters, including their power density, efficiency, EMI suppression, and (eventually) cost. Two of the well-known power converter classes that will be among the biggest beneficiaries of the future availability of M-BD switches are matrix converters (MCs) and current-source inverters (CSIs). While the appealing advantages of matrix converters for direct ac-ac power conversion have long been recognized, the unavailability of M-BD switches has prevented MCs using the baseline 3x3 matrix of ac switches from achieving wide commercial success. After reviewing the basic concepts associated with matrix converters and their control, attention will be focused on the opportunities that M-BD switches open for realizing the full commercial potential of future MCs in applications such as motor drives. Similarly, the future potential of new M-BD switches to revive long-neglected CSI technology will also be explored. Recent work has revealed a variety of appealing properties of CSI-based motor drives in a wide variety of dimensions including high-temperature operation, EMI suppression, and enhanced fault protection in permanent magnet machine drives. After reviewing the basic concepts of CSI technology for both ac-dc and dc-ac power converters, special attention will be focused on the game-changing potential of M-BD switches in future CSI-based integrated motor drives that combine motors and drives into the same housing. Finally, opportunities to realize the full potential of M-BD switches in promising new power converter topologies
will be explored. One particularly exciting development is their application in a new T-Type switching cell (TT-SC) topology that provides the basis for designing high-performance WBG-based three-level voltage-source inverters that can achieve appealingly high power density and efficiency performance metrics. Other promising power converter applications of M-BD switches will also be reviewed. Tutorial participants can expect to leave this tutorial with valuable insights into the emerging technology of M-BD switches and the opportunities they open for more fully exploiting wide-bandgap switch technology to revolutionize the power electronics field.

10AM2 | Interaction Among the Grid-connected Converters through Their Synchronization Mechanism

INSTRUCTORS: Marco Liserre¹, Grahame Holmes², Mario Paolone³, Rongwu Zhu⁴, Roberto Rosso⁵
¹Christian-Albrechts-Universität zu Kiel, Germany; ²RMIT University, Australia; ³Swiss Federal Institute of Technology, Switzerland; ⁴Harbin Institute of Technology, Shenzhen, China; ⁵ENERCON, Germany

Electrical power systems are currently transitioning from centralized structures, where most of the power generation comes from small numbers of large power rotating machines, to distributed structures, where much of the power generation comes from large numbers of small power-electronic-interfaced renewable resources. This increasing use of multi-functional power electronic converters (such as STATCOM, DVR, UPFC, UPS and Solid-state Transformer) is challenging the principles of electrical grid operation. One emerging area of concern is harmonic stability issues created by large numbers of grid-connected power converters, with converter synchronization to the grid emerging as a critical factor which can cause anomalous interactions between these converters. This tutorial considers this synchronization issue from the perspective of both the electrical power grid and the converter. The tutorial begins with the electrical grid perspective, looking at limitations of conventional grid modelling approaches in the presence of gridconnected converters, and then presenting advances that have been made in this area. Next, the tutorial reviews grid-synchronization from the converter perspective, looking at modelling issues, established gridsynchronization strategies and grid converter classification implications. The tutorial then presents power synchronization as an emerging alternative for Grid-Forming-Converters, and considers its benefits in comparison to a Phase-Locked-Loop approach. Finally, the tutorial presents the idea of selfsynchronization, where the converter synchronizes to internal control loop variables instead of measured voltages, to improve its stability and ride-through capability in the presence of grid harmonics and faults.

10AM3 | Wide Bandgap Power Electronics Based Electric Machine Drives

INSTRUCTORS: Jin Wang¹, Yousef Abdullah²
¹The Ohio State University, United States; ²Kuwait University

The demand on high performance high power density electric machine drive systems continues to grow as multiple industry sectors look to cut costs and improve power density and efficiency. For this reason, researchers and engineers have been working on wide bandgap (WBG) based power electronics circuits to meet the immediate needs of industry and satisfy future requirements. The following tutorial provides an in-depth look of challenges and status or WBG motor drives, covering topics on both circuit level and system level, which includes gate drive design, circuit layout, reflective wave, thermal design, EMI, leakage current, and insulation stress to motor windings with high dv/dt PWM. Two case studies, one on a 1.8 kVA integrated GaN based motor drive and the other on 7 kV 1 MVA SiC based motor drive, will be used as examples during the discussion.
Monitoring Power Module Degradation via Lifetime-Varying Parameters

INSTRUCTORS: Christoph H. van der Broeck\textsuperscript{1}, Timothy A. Polom\textsuperscript{2}
\textsuperscript{1}FEV Europe GmbH, Germany; \textsuperscript{2}Silicon Austria Labs GmbH, Austria

This tutorial introduces and breaks down thermal response monitoring and degradation diagnosis techniques especially applicable to power electronic modules. Such monitoring technologies are essential in next-generation integrated power electronic systems that require reduced size, weight and cost while ensuring highly reliable operation over ever extending lifetimes. The introduced methodology ultimately enables realization of predictive maintenance strategies that utilize power converters fully until critical degradation thresholds are reached. They allow operating lifetimes of power conversion systems to be maximized or optimized according to engineering and availability constraints. Degradation mechanisms in power electronic modules occur at material interfaces in large part due to mismatches in thermal expansion coefficients. Resulting thermal cycles periodically induce strain at these material interfaces which, in turn, leads to fatigue, i.e., crack growth and delamination. In many cases, the growth of cracks and delamination occurs within the primary heat dissipation path of a power device, thus altering thermal response. The presented scheme of detection, localization, and overall quantification of different degradation mechanisms is based on systematically monitoring lifetime-varying electrothermal response properties and parameters. This tutorial has the objective to present and clarify details of the many promising research approaches that have been recently proposed to realize in situ health monitoring and diagnosis. After establishing motivation, this tutorial reviews electrothermal modeling approaches especially applicable to power modules and examines typical electrothermal response characteristics. Moving forward, it details thermal real-time monitoring systems that combine temperature data, extracted via sensors and temperature-sensitive electrical/optical parameters, with 3-D real-time models. It is demonstrated how the fusion of model and sensor data can detect abnormal thermal responses, which are linked to occurrences of degradation. Page 2 of 3 The tutorial provides an overview on thermal characterization technologies that measure thermal impedance in time and frequency domain. It is analyzed how thermal impedance reflects different degradation mechanisms and can serve as the focal point of a scheme to localize and quantify these mechanisms. Addressing the broad array of implementation options, the final part of the tutorial investigates real-time technologies that can extract life-time varying thermal parameters, in particular thermal impedance, during normal converter operation. It finally shows how extracted degradation sensitive data can be utilized for degradation diagnosis using artificial intelligence technologies.

Advances in Intelligent Solid-State DC Substations for Future Interconnected DC Grids

INSTRUCTORS: Rik W. De Doncker, Jingxin Hu, Shenghui Cui, Johannes Voss, Philipp Joebges
RWTH Aachen University, Aachen, Germany

The change of the electrical supply system to more environmental-friendly energy sources require the development of a new grid infrastructure. Next to the increasing penetration of large-scale renewable energy sources such as offshore wind farms, the change of consumer behavior in the distribution grids from consumer into producer due to the installation of decentralized generations will result in a bottleneck in the distribution grids. Local or regional balancing between loads and generations is found to be a cost effective solution for the future electrical distribution grids, which can be realized with direct current (DC) technology more efficiently and flexibly. The tutorial will focus on the latest advances and best practices of intelligent solid-state DC substations for future flexible DC grids, which covers a selection of key enabling technologies from converter topologies, optimized control, hardware-in-the-loop simulation techniques, to the development of megawatt mediumvoltage demonstrators. The presented advances are collected from a number of recent and ongoing research projects in the Institute for Power Generation and Storage Systems, which includes the Flexible Electrical Networks (FEN) Research Campus funded by the German Federal Ministry of Education and Research. Following a general introduction and a technology roadmap of flexible DC grids, the tutorial will elaborate the technologies of bidirectional isolated DC-DC converters for intelligent solid-state DC substations for...
interconnection of DC grids at different voltage levels. It will start with dual-active bridge (DAB) based DC-DC converter topologies for LVDC-MVDC applications, where advanced modulation and control of DAB converters will also be discussed. This includes the instantaneous flux and current control method as well as the advanced black start-up and fault ride-through strategies for a highly dynamic and robust operation under both normal and fault conditions. Then, the development and control of an IGCT-based 5 kV, 5 MW DAB DC-DC converter will be presented. This includes the employment of the modified auxiliary-resonant commutated-pole circuit to ensure zerovoltage soft-switching of IGCT devices with snubber capacitors, and a novel anti-saturation detection and compensation methods for medium-frequency transformers. Considering the numerous benefits of the bipolar DC distribution, the tutorial will also present advances in power conversion technologies for bipolar LVDC and MVDC distribution systems. The concept of topological integration is adopted and demonstrated forstate-of-the-art AC-DC and DC-DC converterson such as MMC and DAB, which enables a full bipolar operation capability on the DC side with a minimum count of additional components. The last section of the tutorial will deal with the real-time simulation and hardware-in-the-loop test for intelligent DC substations. Different modeling techniques will be discussed and compared with best practices in the real-time environment. A successful example of using the rapid-control-prototyping tool to develop a high-power medium-voltage modular DC-DC converter will be presented as well as the lessons learnt.

11AM3 | Optimised Electrical Machine Designs for E-Mobility Applications

INSTRUCTORS: Mircea Popescu¹, Philip Mellor², Nick Simpson², James Goss, Melanie Michon¹, Jonathan Godbehere¹

¹Motor Design Ltd., United Kingdom; ²University of Bristol, United Kingdom

Transport electrification is seen as one of main solutions to reduce global CO2 emissions and increased demand of mechanical energy can be provided by electrical energy. The best energy conversion systems are undoubtedly the combination: electrical machines + power electronics + batteries. The increasing demand of full electric vehicles arises specific challenges in terms of design for manufacturing, low weight, material costs and material supply chain. There is a strong interest to reduce the volume and cost of active materials in propulsion motor technologies beyond their current state-of-art, with a strong focus on industrial feasibility for mass production. Potential solutions include increased motor speeds and higher pole numbers and/or the adoption of rare earth free typologies such as reluctance (switched and synchronous) and induction machines. As there can be significantly different usage and performance requirements across e-mobility applications adopting a common standard of motor design is unlikely to yield the optimum in terms of overall system efficiency and electric vehicle range. These considerations will be discussed and compared. Advances in fast switching power semiconductor devices and digital control have enabled high frequency operation of electrical machine drives, with fundamental operating frequencies exceeding 1 kHz being proposed. High frequency operation allows for greater mechanical speeds and designs with a larger number of magnetic poles, leading to a more compact electrical machine package for a given output requirement. However, high frequency operation results in a higher volumetric loss in he active components of the electrical machine; primarily as a result of induced circulating eddy currents in the stator laminations, winding conductors and the rotor. The non-uniform heating and reduction in efficiency associated with these AC loss effects represent a major hurdle to the successful development of compact high frequency electrical machine drives. The benefits and challenges associated with high frequency operation of electrical machines for aerospace and automotive applications will be reviewed and the techniques and design choices available to the designer to reduce high frequency loss effects and extract heat will be surveyed. Cutting-edge sensitivity analysis and multi-objective optimisation techniques will be applied in the design of an electric motor for a PHEV traction application. Each candidate solution will be evaluated in terms of electromagnetic, thermal and mechanical behaviour across the full operating envelope. The optimisation will generate a pareto front which allows efficiency over a drive cycle to be traded off against motor cost. This approach utilises a high performance or cloud computing infrastructure to deliver a truly revolutionary design workflow.
**11AM4 | Conducted, Near-Field and Radiated EMI Emission Mitigation for Wide Bandgap Converters: Fundamentals, Modeling and Solutions**

**INSTRUCTORS:** Cong Li¹, Shuo Wang²

¹GE Research, United States; ²University of Florida, United States

This seminar is part of a series of education activities initiated by IEEE EMC Society Special Committee 5 (SC5) – Power Electronics EMC. The purposes are to raise broader power electronics audiences’ awareness of EMC, and connect advanced EMC technologies with WBG power electronics systems to enable low noise, high efficiency, and high power density solutions for future power conversion systems. This seminar is a comprehensive guide to provide engineers with techniques to develop and construct electromagnetically compatible Wide Bandgap (WBG) power electronic converters. The seminar provides a good opportunity for the fundamental theory, measurement, and suppression of electromagnetic interference (EMI) for WBG power electronics. It will have full coverage on the conducted emission, near field, and radiated EMI. The first section provides EMC theory and fundamentals for WBG power conversion, comparison of commonly used industrial EMI standards, techniques for common-mode (CM) and differential mode (DM) current separation, and practical CM/DM measurement methods. The second section focuses on a comprehensive 5-step EMC design process for WBG power converters’ conducted emission EMI challenges: "SOLVE". SOLVE design flow begins with considerations on Selecting proper architectures based upon system ratings and EMC specifications. The next steps develop techniques for Obtaining component parasitics and Layout for the system EMI model. Vetting of different filter design aspects, including magnetic material selection, structure, and practical filter performance. The last step presents techniques and principles for packaging Enhancement. The third section focuses on the near field EMI generated from components in WBG power converters. The near field EMI can be generated from WBG power modules, transformers, inductors, and PCB traces. It can be coupled to other components in the converter to deteriorate the WBG converter’s conductive and radiated EMI. With the high switching speeds and high switching frequencies of WBG power electronics to improve power density, the components are very close to each other, therefore the importance of understanding, measurement, and reduction of near field EMI cannot be overemphasized. The seminar will focus on the advancement of the theory, identification, and reduction of the near field EMI for WBG power electronics. The fourth section focus on the radiated EMI for WBG power electronics. The WBG device powered power electronics systems can achieve higher power densities than those with the 2 conventional Si devices. However, higher switching speed and higher switching frequencies lead to more significant radiated EMI. The radiated EMI can be over the limits from several to hundreds of MHz, which poses a big barrier to high power density power electronics design in the areas such as consumer electronics, electric vehicles, and the aviation industry, etc. This is especially important for most power electronics engineers who lack knowledge on the radiated EMI in power electronics systems. The seminar will focus on the advancement of the theory, measurement, and reduction techniques developed in recent years for the radiated EMI in WBG power electronics systems.

**12PM1 | Applying Artificial Intelligence to Battery State Estimation**

**INSTRUCTORS:** Carlos Vidal¹, Phillip Kollmeyer¹, Javier Gazzarr²

¹McMaster University, Canada; ²MathWorks, United States

This tutorial will teach the entire process necessary to create, test, and deploy machine learning algorithms which estimate battery state of charge or other states and properties of interest. Traditional state estimation methods depend on battery models that cannot always capture the complex nonlinear, temperature dependent characteristics inherent in battery electrochemistry. Machine learning simplifies the process by treating the battery as a black box. The relation between states of interest and measured battery parameters are fit to a neural network, which is a series of matrix calculations containing hundreds or thousands of learnable parameters. Machine learning has potential to achieve better accuracy than conventional battery modeling and state estimation techniques and is a promising solution for a wide range of commercial and industrial applications. Battery state estimation and machine learning theory will first be introduced, along with a discussion of different types of machine learning methods that can be
applied to battery applications. Then the steps to create a machine learning algorithm – an experimental collection of data, defining network configuration and hyperparameters, training, and testing – will each be discussed in detail. A case study comparing a non-recurrent feedforward neural network with a recurrent, LSTM-based neural network will be provided, demonstrating that both network types can perform very well for realistic vehicle drive cycles over a wide range of temperatures. The tutorial will also include an interactive session where the attendees will train and test neural networks for the state-of-charge estimation via example data and code executing in MATLAB Online, which can run in any web browser. Following the training and testing process, the next step is to deploy the networks in hardware and evaluate their real-time performance and computational load. The method of saving a neural network as a MATLAB object for direct use in Simulink, auto-generation of C-code, and deployment to a real-time target, such as a battery management system, will be demonstrated. Example results showing processor time as a function of algorithm type and several learnable parameters will be given. It will provide insights into machine learning algorithms’ suitability for large battery packs with a hundred or more cells. The tutorial will also provide several other examples of how to apply machine learning to battery applications, including (1) use of machine learning in place of equivalent circuit models, (2) estimation of temperature to reduce the need for physical sensors in a battery pack, and (3) state of health estimation.

12PM2 | Resiliency-Oriented Grid-Interactive Converters: Concepts, Design, and Field Implementation

INSTRUCTORS: Xiaonan Lu¹, Jin Tan², Andy Hoke², Lisa Qi³
¹Temple University, United States; ²National Renewable Energy Laboratory, United States; ³ABB Inc., United States

As the penetration level of inverter-based distributed energy resources (DERs) increases rapidly, distribution grids, as the most significant ‘grid-edge’ for DER integration, play a crucial role in bridging the grid backbone (i.e., transmission system) to the end-users. Resilient and stable distribution grids are urgently needed to modernize electric power grids with high penetration of inverter-based resources (IBRs) and ensure operational continuity. Conventional grid-interactive power electronic converter design mainly focuses on satisfying the design constraints of individual converter units and the operational requirements at the single point of interconnection (POI). However, given the increasing penetration level of IBRs in modern power grids, converter systems should also be taken into account to meet the grid needs in a wider area. Furthermore, the concept of converter design has been tremendously advanced, considering the cross-domain and multi-disciplinary objectives. Particularly, on top of the conventional and legacy converter design constraints on power density, energy conversion efficiency, among others, additional considerations on the interactions among multiple converters (i.e., converter systems) should be highlighted, with special emphases on the coupling operation between converter hardware implementation (physical layer) and information exchange through communication interconnections (cyber layer), control design respecting the tradeoff between local control constraints satisfaction and interactive operation with neighboring converters, and fault-tolerant design and converter system resiliency enhancement coordinated with conventional protection schemes in a multi-timescale context. All these emerging design constraints call for a paradigm shift into a resiliency-oriented converter design framework. In this tutorial, the diversified and multi-disciplinary instructor team from academia, government national laboratories, and leading industry companies will introduce the resiliency-oriented modeling and control of grid-interactive converter, and the topics will range from fundamental concepts covering the necessary background knowledge to advanced applications and field deployment. The topics will echo the cutting-edge technologies and applications of grid-interactive converters, including hybrid and networked AC and DC microgrids, inverter-based renewable energy (e.g., photovoltaics) integration, resiliency enhancement, and protection coordination in inverter-dominated power grids, among others.
### 12PM3 | Cryogenic Power Electronics Design for Electrified Aircraft Propulsion

**INSTRUCTORS:** Fei (Fred) Wang¹, Zheyu Zhang², Ruirui Chen¹, Shengyi Liu³  
¹University of Tennessee, United States; ²Clemson University, United States; ³Boeing Company, United States

Cryogenic power electronics offer numerous game-changing benefits, including 1) improved performance of power semiconductor devices, such as silicon (Si)- and gallium nitride (GaN)- based, offering decreased specific on-state resistance and increased switching speed; 2) faster switching frequency operation at cryogenic temperature, greatly reducing the need for passive (e.g. EMI filtering); thereby reducing filter weight; 3) less cooling requirement at extremely low ambient temperatures, and 4) light and/or efficient busbar designs due to the low resistivity of conductors at cryogenic temperature. This seminar will provide several key perspectives for the cryogenic power electronics design from the component up to the converter level. First, the characteristics of critical components, including power semiconductors and magnetics, at cryogenic temperature are introduced. Second, special considerations, trade, and design studies of cryogenic power stage and filter are discussed. Then, two examples of a 40 kW Si-based and a 1 MW SiC-based cryogenically-cooled inverter system for electric aircraft propulsion are illustrated, with cooling design, safety considerations, and the protection scheme highlighted. Upon completion, seminar attendees will have a firm grasp on the cryogenic power electronics design and be provided with a range of possible options in order to better utilize the cryogenic cooling system in power converters.

### 12PM4 | Hybrid Semiconductor Switches based Power Modules, Converters, and Systems

**INSTRUCTORS:** Fang Luo¹, Jiangbiao He²  
¹State University of New York at Stony Brook, United States; ²University of Kentucky, United States

Power semiconductor devices play a backbone role in the development of power electronics. Over the past decade, semiconductor devices have experienced rapid development, especially promoted by the emerging wide-bandgap device technologies. However, regardless of conventional Silicon (Si) devices or the new Gallium Nitride (GaN) and Silicon Carbide (SiC) devices, every type of semiconductor devices has its own pros and cons. To fully leverage their different characteristics, various hybrid power devices and modules have been proposed and developed in the past years, ranging from the device level to converter level, or even power electronics system level. As a matter of fact, many of the concepts of hybrid semiconductor switches have been successfully applied in industries, such as the monolithic integration of Si IGBTs and SiC anti-parallel diodes (i.e., co-pack), the Cascode JFET, and the “Si+SiC” hybrid three-level active neutral-point-clamped commercial modules. Numerous performance benefits have been achieved with such hybrid devices, modules, and converters, including efficiency and reliability improvement, EMI mitigation, cost reduction, etc. In this tutorial, we will start with a review of the development history of various hybrid semiconductor devices and the related successful commercial examples, followed by the new opportunities and challenges with the emerging wide bandgap devices. Afterwards, we will present “Si+SiC” hybrid switching devices at the power module level, review the operating principle, gate drivers, packaging, and performance evaluation. Furthermore, we will present various “Si+SiC” hybrid power converters, specifically including the hardware development, controls, and experimental verifications. Application examples include electric aircraft propulsion and interruptible power supplies for data center applications will be discussed. Finally, we will conclude the tutorial with a summary and Q&A session.
Design and Development of Scalable Battery Testers/Emulators and Their Applications for Future Transportation Electrification

INSTRUCTORS: Sheldon Williamson\(^1\), Uday Deshpande\(^2\)
\(^1\)Ontario Tech University, Canada; \(^2\)D&V Electronics Ltd., Canada

As electrified transport systems proliferate, batteries are increasingly becoming the critical element in the immediate and long-term technical and commercial success of these programs. Understanding of battery technology and its role in the applications is becoming crucial. While there have been many articles published on battery elements and systems, this tutorial approaches the problem from a user mindset. How do we use the various technology elements of battery models, calculations of charge and health to ensure a successful design outcome? Additionally, this tutorial will discuss how these elements apply to test and validation. Very few battery cell models are available in literature; most models are very generic or basic. Cell models are either based on equivalent circuit parameters, such as RC, R-RC, or based on SOC calculation - these are not enough to measure Ah-capacity fade, state-of-health (SOH), and/or end-of-life (EOL). Thermal modeling is used minimally in related literature for testers and emulators. Cell degradation assessment due to varying temperature gradients is not feasible - this is particularly true for fast charging applications; 1C and above. Hence, testers/emulators today are unable to provide boundary testing (near EOL), specifically for applications such as fast chargers and controller validation or BMS validation. This tutorial will present more realistic and practical electro-chemical and electro-thermal models for emulation and testing purposes. Performance modeling will be presented in order to test advanced machine learning (ML)-based battery management systems (BMS) and charger controllers. Stochastic models of battery cells will also be presented in this tutorial. In addition, SOC/SOH estimation models specific to emulator/tester applications, which affect instantaneous battery performance, will also be presented. Advanced thermal/EOL degradation models, which can be used to test BMS with battery health estimation and energy management, will also be presented for testing applications. Finally, the tutorial will include emulation of custom cell models and health-conscious fast charging algorithms, keeping the effects of temperature gradients in mind. Keeping these aspects in mind, the following key practical aspects will be presented: • Emulation at cell-level, module level, and pack level (including development of advanced, higher-order thermal models); • Estimation of SOC and SOH using advanced ML techniques; • Validation of BMS developmental steps with respect to hardware, firmware, and software; • Testing of newly developed algorithms (balancing, SOC, SOH, EOL, etc.); • Development of a new constant-temperature-constant voltage (CT-CV) algorithm; • Testing of fault cases (over-temperature, over-voltage, etc.); • Programmable standard and user-owned battery models; • Bidirectional power supply design (regenerative design) for high-power discharge; Constant-Voltage (CV), Current (CC), Power (CP), Series Resistance (CR) loading; • Real-time HIL (Simulation of high-voltage batteries at cell/module level; Real-time multi-cell battery simulations; Temperature simulations using isolated analog outputs and advanced models).

Printed Circuit Boards in Power Converter Applications: Design Considerations and Failure Mechanisms

INSTRUCTORS: Ashish Arora, Yike Hu
Exponent, United States

Printed Circuit Boards (PCBs) are the backbone of all electronic circuits and are ubiquitous in today’s world in almost all applications. PCBs not only interconnect components through conductors routed through the board with traces and vias, but also provide electrical insulation between conductors of different potentials that are in different circuit nodes. Their use in power converter and energy storage applications gives rise to risks that do not necessarily exist in other lower power applications. While PCB failures are not very common, a propagating PCB failure in a power converter or energy storage application can trigger a cascading series of failures that spread to the system’s energy storage component eventually resulting in a fire. Propagating PCB failures can occur due to a number of reasons such as contamination on the PCB, improper PCB layout or a failure of a component on the PCB itself. Design choices made during the development of a product, the cleanliness of the PCB manufacturing
process, the stresses applied to the PCB during a products assembly process etc. can all impact the probability of a PCB failure in the field. Understanding the causes of PCB failures and how these failures can propagate in an application allows for the design and manufacture of systems with more robust PCBs that have a lower probability of a catastrophic failure in the field. This tutorial will provide an overview of some of the requirements and challenges of designing and manufacturing PCBs specially for power converter and energy storage applications. The tutorial will also provide an overview of the types of PCBs, how PCBs are manufactured, how components get onto the PCBs and the standards that exist to evaluate the PCB manufacturing process. PCB failure mechanisms such as interconnect overheating, contamination, electrochemical migration, dendrite formation and conductive anodic filaments, tin whiskers and component over-heating will be reviewed. Case studies will provide examples of failures observed in the field and the means to mitigate them.

1PM3 | Defining, Modeling, and Optimizing for Energy Efficiency in 5G

INSTRUCTOR: Brian Zahnstecher
PowerRox, United States

With so much hype and news around the deployment of the next-generation broadband network (5G), it is quite alarming to find out much of the “promise” of 5G assumes the energy infrastructure exists to power all this new HW. More fundamentally, the payback estimates for these massive investments all assume the network can be fully utilized, while neglecting network bottlenecks because they relate to energy and not data throughput. The 5G Energy Gap and potential electrical grid destabilization is a risk to the entire deployment and all that is attached to it. This risk is broken down into the concepts of the Power Value Chain, Power Cost Factor, 5G Derate Factor, and other technical/business and even socioeconomic factors. This entry/intermediate-level seminar introduces these concepts in a simple, yet realistic way to break the complicated network down into manageable pieces for all stakeholders, then translate the unique inputs/requirements of each into the normalized, “universal currency” of energy. From there, both static and dynamic analyses can be performed to assess end-to-end network configurations and optimize each piece through the lens of energy efficiency.
Technical Program

The following Sessions cover all areas of technical interest to the practicing energy conversion professional.

Ask the authors questions using the Chat located next to each pre-recorded video.

**Topic A: Renewable and Sustainable Energy Applications**

**A01: Photovoltaic Systems**

1772 | **Comparison of Modulation Techniques for a Single-Phase Full-Bridge Photovoltaic Micro-Inverter considering Reactive Power Capability**
Tobias Brinker, Lennart Hoffmann, Jens Friebe
Leibniz Universität Hannover, Germany

1294 | **Experimental Analysis of Laminated Bus Bars for Building-Integrated Photovoltaic Applications**
S. Ravyts1, P. Nivelle2, J. Carlous2, R. Sabariego1, M. Daenen2, J. Driesen3, J. Cappelle1
1KU Leuven - Gent, Belgium; 2Hasselt University, Belgium; 3KU Leuven- Leuven, Belgium

1779 | **Influence of DC/DC Stage on the Design of the Output Filter of the Inverter Stage in Two-Stage Grid-Connected PV Systems**
Branislav Stevanović1, Santiago Cóbrecs2, Emanuel Serban3, Pedro Alou1, Martin Ordonez2, Miroslav Vasić3
1Universidad Politécnica de Madrid, Spain; 2Universidad de Alcalá, Spain; 3The University of British Columbia, Canada

1362 | **Sizing Approach for a Single-Phase Grid-Connected Photovoltaic Converter with Active and Reactive Power Management**
Rosa Iris Viera-Díaz, Mario González-García, Ricardo Álvarez-Salas, Homero Miranda, Yuniel León-Ruíz
Universidad Autónoma de San Luis Potosí, Mexico

**A02: Renewable and Sustainable Energy Systems**

2422 | **Economic Analysis for Hourly Dispatching Wind Energy Power Using Battery and Supercapacitor Hybrid Energy Storage System**
Pranoy Roy, JiangBiao He
University of Kentucky, United States

1819 | **A Single-Phase Enhanced Grid-Forming Controller with Converter Current Limiting**
Masoud Karimi-Ghartemani1, Ali Zakerian1, Sayed Ali Khajehoddin2
1Mississippi State University, United States; 2University of Alberta, Canada

1226 | **Comparative Study of Transverse Flux Permanent Magnet Machines for Wind Power Applications**
R. Kumar1, Z. Q. Zhu1, A. Duke2, A. Thomas2, R. Clark2, Z. Azar2
1The University of Sheffield, United Kingdom; 2Sheffield Siemens Gamesa Renewable Energy Research Centre, United Kingdom

2346 | **Review of DC Offshore Wind Farm Topologies**
Kareem A. Noor Al-Deen, Hussain A. Hussain
Kuwait University, Kuwait

1990 | **Control Strategies for Variable Speed Operation of Pumped Storage Plants with Full-Size Converter Fed Synchronous Machines**
Raghbendra Tiwari1, Roy Nilsen1, Olve Mo2
1Norwegian University of Science and Technology, Norway; 2SINTEF Energy Research, Norway
### A03: Power Converters for Renewable and Sustainable Energy Systems

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<td>Flexible AC Phase Configurable NPC-Based Converter Topology</td>
<td>Emanuel Serban(^1,2), Jan Hammer(^1), Cosmin Pondiche(^3), Martin Ordonez(^1)</td>
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<td>(^1)The University of British Columbia, Canada; (^2)EnerSys, Canada</td>
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<td>Harmonics Compensation of the LCC in a Parallel LCC-VSCs Configuration for a Hybrid AC/DC Network</td>
<td>Rouzbeh Reza Ahrabi, Yunwei Li (^1) University of Alberta, Canada</td>
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<td>1192</td>
<td>A Dual-Input Single-Output DC-DC Converter Topology for Renewable Energy Applications</td>
<td>Pasan Gunawardena, Nie Hou, Dulika Nyanasiri, Yunwei Li (^1) University of Alberta, Canada</td>
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<td>PWM Control of n-Phase Interleaved Current Fed Topology</td>
<td>Sonam Acharya(^1), Santanu Mishra(^1), Arvind Tiwari(^2)</td>
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<td></td>
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<td>(^1)Indian Institute of Technology Kanpur, India; (^2)GE Research, United States</td>
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### A04: Control of Photovoltaic Systems

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<td>Nidhi Malhotra, Pawan Kumar, R.K. Singh (^1) Indian Institute of Technology (BHU), Varanasi, India</td>
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<td>Jinkui He(^1), Ariya Sangwongwanich(^1), Yongheng Yang(^2), Zhongyi Quan(^3), Yunwei Li(^3), Francesco Iannuzzo(^1)</td>
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<td>(^1)Aalborg University, Denmark; (^2)Zhejiang University, China; (^3)University of Alberta, Canada</td>
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<td>Zhaoxia Yang(^1), Jianwu Zeng(^1), Qixing Ren(^2), Liangcai Wu(^2), Zhengjun Liao(^2)</td>
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<td>(^1)Minnesota State University, United States; (^2)Growatt New Energy Technology Co., Ltd., China</td>
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<td>Angelo G. Santos, Filipe A.C. Bahia, Fabiano F. Costa, André P.N. Tahim, Leandro L.O. Carralero</td>
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<td>Federal University of Bahia, Brazil</td>
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<td>Chu Wang, Min Chen, Yufei Jie (^1) Zhejiang University, China</td>
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<td>(^1)Wuhan University, China</td>
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2053 | Cell Balancing of Li-Ion Battery Pack with Adaptive Generalised Extended State Observers for Electric Vehicle Applications
Utkał Ranjan Muduli¹, Khaled Al Jaafari¹, Khalifa Al Hosani¹, Ranjan Kumar Behera², Rustem R. Khusnutdinov³, Alfred R. Safin³
¹Khalifa University, United Arab Emirates; ²Indian Institute of Technology Patna, India; ³Kazan State Power Engineering University, Russia

1666 | Passivity Control in Modular Battery Energy Storage Systems
Ezequiel Rodriguez¹, Ramon Leyva², Gaowen Liang¹, Glen G. Farivar³, Josep Pou¹, Christopher D. Townsend⁴, Naga Brahmendra Yadav Gorla³
¹Nanyang Technological University, Singapore; ²Univrsitat Rovira i Virgili, Spain; ³University of Western Australia, Australia

1299 | A Comparison of the Battery Fault Tolerance of Modular Multilevel Converters with Half-Bridge and Full-Bridge Submodules
Gaowen Liang¹, Glen G. Farivar¹, Gorla Naga Brahmanreda Yadav¹, Christopher D. Townsend², Salvador Ceballos³, Hossein Dehghani Tafti³, Josep Pou¹
¹Nanyang Technological University, Singapore; ²University of Western Australia, Australia; ³Ikerlan Technology Research Centre (BRTA), Spain

1565 | Design, Control and Prototyping of a Bidirectional Dual Active Bridge Converter for Integrating a Sodium Metal Halide Battery into a Telecom Station
Mario Porru¹², Alessandro Serpi¹², Alessandro Soldati¹, Luca Tassi¹, Alfonso Damiano²
¹NEPSY srl, Italy; ²University of Cagliari, Italy; ³University of Parma, Italy

1794 | Design of a 1500V Si IGBT/SiC MOSFET Hybrid Switch-Based Three-Level Active NPC Inverter
Haichen Liu, Tiefu Zhao, Jim Gafford, Somasundaram Essakiappan, Madhav Manjrekar
University of North Carolina Charlotte, United States

1894 | A Novel ZCS Bidirectional CUK Equalizer for Energy Balance of Battery Cells Connected in Series
Xinchi He¹, Rui Ling¹, Dongxue Li²
¹Chongqing University, China; ²Vicor Corporation, United States

1604 | Novel Battery Equalizer-Charger Symbiosis Structure Based on Three-Port DC-DC Converters
Nguyen-Anh Nguyen, Phuong-Ha La, Ngoc-Thao Pham, Sung-Jin Choi
University of Ulsan, Korea

1089 | Solar Water Pumping System with Captive Energy Storage Functionality
Hina Parveen, Utkarsh Sharma, Bhim Singh
Indian Institute of Technology Delhi, India

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Indian Institute of Technology Bombay, India

2215 | Subsynchronous Control Interaction Study Framework and Applications to Southern California Edison System
Chaoyang Jing, Patricia Arons
Southern California Edison, United States
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Mohd Aquib, Suryanarayana Doolla, Mukul C. Chandorkar
*Indian Institute of Technology Bombay, India*

**2248 | Design and Optimization Strategy to Size Resilient Stand-Alone Hybrid Microgrids in Various Climatic Conditions**
Norma Anglani¹, Giovanna Oriti², Ruth Fish², Douglas L. Van Bossuyt²
¹University of Pavia, Italy; ²Naval Postgraduate School, United States

**1807 | Dual Converter Operating with Floating Capacitors Connecting Open-End Winding Doubly-Fed Induction Generator to a DC Microgrid**
Emerson L. Soares¹, Cursino B. Jacobina¹, Nady Rocha³, Victor Felipe M.B. Melo²
¹Federal University of Campina Grande, Brazil; ²Federal University of Paraíba, Brazil

**1246 | A New General Multi-Layout Energy Hub Management Model for Industrial and Commercial Multi-Energy Systems with Complex Configurations**
Mehrdad Aghamohamadi¹, Clement Chuah¹, Amin Mahmoudi¹, John K. Ward², Mohammed H. Haque³
¹Flinders University, Australia; ²CSIRO, Australia; ³University of South Australia, Australia

**2307 | Soft Switched High Gain Boost Converter for Low Voltage Applications**
Manikant Kumar, Kirti Mathuria, Vinod Kumar Yadav, Arun Kumar Verma
*Malaviya National Institute of Technology Jaipur, India*

**2132 | A Fully Symmetrical Three-Port Hybrid Converter for PV Systems**
Zhongting Tang¹, Yongheng Yang¹, Frederic Blaabjerg¹
¹Aalborg University, Denmark; ²Zhejiang University, China

**1098 | Experimental Verification of Three-Phase PV Inverter Using Multiple Bidirectional Choppers for Utility-Scale PV Systems**
Linyue Qiao, Yoshifumi Shimizu, Makoto Hagiwara
*Tokyo Institute of Technology, Japan*

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¹Eaton, United States; ²Eaton, China

**2567 | Design and Testing of a Modular Back-to-Back Power Electronics Converter for Wave Energy Harvesting**
Mattia Mantellini¹, Riccardo Morici¹, Marcos Blanco², Marcos Lafoz², Gustavo Navarro², Luca Zarri³
¹OCEM Power Electronics, Italy; ²CIEMAT, Spain; ³University of Bologna, Italy

**2447 | Design Considerations of 6.5kV Enabled Three-Level and 10kV Enabled Two-Level Medium Voltage SST**
Apoorv Agarwal, Anup Anurag, Nithin Kolli, Ashish Kumar, Subhashish Bhattacharya
*North Carolina State University, United States*
1060 | Investigation of a New Alternate Arm Modular Multilevel Converter Topology for HVDC Applications
Dereje Woldegiorgis, Alan Mantooth
University of Arkansas, United States

1980 | A Hybrid GaN + Si Based Cascaded H-Bridge Multi-Level Inverter and PWM Scheme for Improved Efficiency
Prince Kumar1, D. Venkatramanan1, Abhijit Kshirsagar2, Ned Mohan1
1University of Minnesota Twin Cities, United States; 2Indian Institute of Technology Dharwad, India

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Honorio Martinez Sarmiento1, Maen Marji1, Cheaheng Lim1, Jonghoon Kim2, Nan Wang1, Woonki Na1
1California State University Fresno, United States; 2Chungnam National University, Korea

Universidad Tecnológica de la Mixteca, Mexico

1200 | A New Kalman-Filter-Based Harmonic Current Suppression Method for the Virtual Oscillator Controlled Voltage Source Converters with LCL
Siyi Luo1, Weimin Wu1, Koutroulis Efthychios2, Frede Blaabjerg3, Henry Shu-Hung Chung4
1Shanghai Maritime University, China; 2Technical University of Crete, Greece; 3Aalborg University, Denmark; 4City University of Hong Kong, China

1300 | Transient Stability Enhancement for Virtual Synchronous Generator by Combining Direct Power Control
Xuejiao Zhong1, Yutao Lou2, Tiliang Wen1, Donghai Zhu1, Xudong Zou1, Xiang Guo1
1Huazhong University of Science and Technology, China; 2Shanghai Institute of Satellite Engineering, China

2334 Power-Synchronized Current Control for Grid-Connected Converters
Xiao Wang, Xiongfei Wang
Aalborg University, Denmark

1587 | A New Control Strategy Based on PLL to Enhance System Stability under Varying Output Power in Weak Grids
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1Chongqing University, China; 2State Grid Chongqing Electric Power Company, China

1323 240°-Clamped PWM in Three Phase Grid-Connected PV Converter Application
Hafsa Qamar, Haleema Qamar, Rajapandian Ayyanar
Arizona State University, United States

Session A09: Grid Integration of Renewables

1656 | An Enhanced Double Quasi-PR Controller for Grid-Side Inverter with Long Transmission Cable
Weibiao Wu1, Ke Hu2, Ming Zhang1, Gujing Han1
1Wuhan Textile University, China; 2Huazhong University of Science and Technology, China

1066 | Circulating Current Analysis of Paralleled Grid-Connected Inverters Based on the Multi-Frequency Model
Liguo Wu, Xinbo Ruan, Zhiheng Lin, Hao Zhang
Nanjing University of Aeronautics and Astronautics, China
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<td>¹<em>Christian-Albrechts-Universität zu Kiel, Germany; ²Aalborg University, Denmark</em></td>
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<td>¹<em>Aalborg University, Denmark; ²Shell Global Solutions International B.V., The Netherlands</em></td>
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<td>¹<em>North China Electric Power University, China; ²University of Sargodha, Pakistan</em></td>
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<td>¹<em>Aalborg University, Denmark; ²Power Grids Research Hitachi ABB Power Grids, Sweden; ³Electrical Systems Ørsted Offshore A/S, Denmark</em></td>
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Sanjay Rajendran, Soumik Sen, Zhicheng Guo, Alex Q. Huang
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1University of Arkansas, United States; 2Universidad Nacional de Rio Cuarto, Argentina

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1University of Wisconsin Milwaukee, United States; 2Florida State University, United States
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Pramod Apte¹, Siqi Lin¹, Lukas Fraeger², Jens Friebe¹  
¹Leibniz Universität Hannover, Germany; ²BLOCK Transformatoren-Elektronik GmbH, Germany |
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| **1083** | A Bipolar Hybrid Circuit Breaker for Low-Voltage DC Circuits |
| Sudipta Sen¹, Shahab Mehraeen¹, Keyue Smedley² |
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| Jose David Vidal Leon¹, Andres Tarraso¹, Jose Ignacio Candela¹, Pedro Rodriguez² |
| ¹Polytechnic University of Catalonia, Spain; ²Luxembourg Institute of Science and Technology, Luxembourg |

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| Moazzam Nazir¹, Johan H. Enslin¹, Klaehn Burkes² |
| ¹Clemson University, United States; ²Savannah River National Laboratory, United States |
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## Session C01: Big Data, Machine Learning, Cyber Security

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|            |                                                                      | Kanagawa Institute of Technology, Japan                                                        |

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|            |                                                                      | ¹University of North Carolina Charlotte, United States; ²University of Central Florida, United States; ³Hebei University of Technology, China |
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|            |                                                                      | ¹University of California Santa Cruz, United States; ²Dynapower Company, LLC, United States; ³Naval Sea Systems Command, United States |
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|            |                                                                      | Oak Ridge National Laboratory, United States                                                   |
| 2474       | Detection of Stator Fault in Synchronous Reluctance Machines Using Shallow Neural Network | Siwan Shachin Narayan¹, Rahul R. Kumar¹, Giansalvo Cirrincione², Maurizio Cirrincione³  
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Subham Sahoo, Huai Wang, Frede Blaabjerg  
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Ximu Zhang¹, Yang Huang¹, Jared Walden¹, Hua Bai¹, Fanning Jin², Xiaodong Shi², Bing Cheng²  
¹*University of Tennessee Knoxville, United States; ²Mercedes-Benz R&D North America, Inc., United States*

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Bongwoo Kwak¹, Myungbok Kim¹, Jonghoon Kim²  
¹*Korea Institute of Industrial Technology, Korea; ²Chungnam National University, Korea*

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¹*Soongsil University, Korea; ²Texas A&M University Kingsville, United States*

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¹*Aalborg University, Denmark; ²Zhejiang University, China*

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Siemens Digital Industries Software, Canada; Siemens Digital Industries Software, China

1297 | A Composite Converter with Reduced Power Electronics for Electric Powertrain Applications
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¹University of Arkansas, United States; ²University of Illinois at Urbana-Champaign, United States

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¹Virginia Polytechnic Institute and State University, United States; ²Raytheon Technologies, United States; ³GE Aviation, United States

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Ontario Tech University, Canada

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Mohammad Saleh Khan¹, Soumya Shubhra Nag¹, Anandarup Das¹, Changwoo Yoon²
¹Indian Institute of Technology Delhi, India; ²Seoul National University of Science and Technology, Korea

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¹BRP-Rotax GmbH & Co. KG, Austria; ²Johannes Kepler University Linz, Austria

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¹Norwegian University of Science and Technology, Norway; ²SINTEF Energy Research, Norway
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¹University of Oviedo, Spain; ²Ingeteam SA, Spain

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¹Technical University of Munich, Germany; ²The University of Auckland, New Zealand

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¹University of Dayton, United States; ²Event 38 Unmanned Systems, United States

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¹The University of Akron, United States; ²Bendix Commercial Vehicle Systems, United States

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University of Kentucky, United States

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¹The University of Akron, United States; ²Bendix Commercial Vehicle Systems, United States

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Indian Institute of Technology Kharagpur, India

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¹McGill University, Canada; ²OPAL-RT Technologies, Inc., Canada

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¹Pohang University of Science and Technology, Korea; ²Hyundai Motor Company, Korea

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The University of Akron, United States

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Jaehong Lee¹, Myung-Yong Kim², Seung-Hwan Lee¹  
¹University of Seoul, Korea; ²Korea Railroad Research Institute, Korea

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Yiming Zhang¹, Yuanchao Wu¹, Shuxin Chen², Xin Li², Yi Tang²  
¹Fuzhou University, China; ²Nanyang Technological University, Singapore
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Erik Hoevenaars¹, Marc Hiller²
¹Robert Bosch GmbH, Germany; ²Karlsruhe Institute of Technology, Germany

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Arka Basu, Subhajyoti Mukherjee
Indian Institute of Technology Bhubaneswar, India

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Jufeng Yang¹, Yingfeng Cai¹, Chris Mi²
¹Jiangsu University, China; ²San Diego State University, United States

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Beijing Institute of Technology, China

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Jufeng Yang¹, Yingfeng Cai¹, Chris Mi²
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Marui Li¹, Chaoyu Dong¹,², Yunfei Mu¹, Xiaohong Dong¹, Jingming Cao¹, Hongjie Jia¹
¹Tianjin University, China; ²Imperial College London, United Kingdom; ³Hebei University, China
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Xinrong Huang¹, Siyu Jin¹, Jinhao Meng², Remus Teodorescu¹, Daniel-Ioan Stroe¹  
¹Aalborg University, Denmark; ²University of Electronic Science and Technology of China, China

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Zhan Ma¹, Xiuqing Yi², Wei Li³, Feng Gao¹, Fujia Yu³  
¹Shandong University, China; ²Shandong University of Traditional Chinese Medicine, China; ³State Grid Binzhou Power Supply Company, China

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Nathan M. Ellis, Robert C.N. Pilawa-Podgurski  
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Lee Gill, Luciano A. Garcia Rodriguez, Jacob Mueller, Jason Neely  
Sandia National Laboratories, United States

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Elaine Ng, Jessica D. Boles, Jeffrey H. Lang, David J. Perreault  
Massachusetts Institute of Technology, United States

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Beijing Jiaotong University, China

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Dulika Nayanasiri, Yunwei Li  
University of Alberta, Canada

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Kazuto Takagi, Yuuki Aoyagi, Akiteru Chiba  
GS Yuasa Infrastructure Systems Co., Ltd., Japan

**1761 | Design Optimization of PCB-Winding Matrix Transformer for 400V/12V Unregulated LLC Converter**  
Pranav Raj Prakash, Ahmed Nabih, Qiang Li  
Virginia Polytechnic Institute and State University, United States

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Alejandro Fernandez-Hernandez¹, Asier Garcia-Bediaga¹, Irma Villar¹, Gonzalo Abad²  
¹Ikerlan Technology Research Centre (BRTA), Spain; ²Mondragon Unibertsitatea, Spain

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The University of British Columbia, Canada
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Johannes Voss, Raphael Mencher, Philipp Joebges, Jan Mathé, Rik W. De Doncker
RWTH Aachen University, Germany

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Mandeep Singh Rana, Santanu K. Mishra, Hitesh Kumar
Indian Institute of Technology Kanpur, India

Low-Profile and High-Efficiency 3 kW 400 V-48 V LLC Converter with a Matrix of Four Transformers and Inductors for 48V Power Architecture for Data Centers
Ahmed Nabih, Qiang Li
Virginia Polytechnic Institute and State University, United States

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Abirami Kalathy, Majid Pahlevani, Praveen Jain
Queen’s University, Canada

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Asier García-Bediaga, Ander Avila, Itziar Alzuguren, Alejandro Rujias
Ikerlan Technology Research Centre (BRTA), Spain

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Cornell University, United States

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Solantro Semiconductors Corp., Canada

A CCM Bridgeless Single-Stage Soft-Switching AC-DC Converter for EV Charging Application
Peyman Amiri1, Wilson Eberle1, Deepak Gautam2, Chris Botting2
1The University of British Columbia, Canada; 2Delta-Q Technologies Inc., Canada

An Interleaved Bridgeless AC/DC Stacked SiC Switches Based LLC Converter with Semi-Active Rectifiers for EV High Voltage Battery Systems
Mehdi Abbasi, John Lam
York University, Canada

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Hokkaido University, Japan
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<td>Javad Khodabakhsh, Gerry Moschopoulos</td>
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<td>Hao Wen¹, Dong Jiao¹, Jih-Sheng Lai¹, Johan Strydom², Bing Lu²</td>
<td>¹Virginia Polytechnic Institute and State University, United States; ²Texas Instrument, United States</td>
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<td>Savatore Riccardo Di Salvo¹, Matteo Bulzi¹, Jacopo Riccio², Riccardo Leuzzi¹, Pericle Zanchetta¹, Norma Anglani¹</td>
<td>¹University of Pavia, Italy; ²University of Nottingham, United Kingdom</td>
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<td>Benedikt Riegler, Annette Mütze</td>
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<td>Leon Fauth¹, Christian Beckemeier¹, Jens Fribe¹,²</td>
<td>¹Technische Universität Braunschweig, Germany; ²Leibniz Universität Hannover, Germany</td>
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<td>Massimiliano Biaso, Riccardo Breda, Mattia Iurich, Simone Mazzer, Roberto Petrella</td>
<td>University of Udine, Italy</td>
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<td>¹Florida State University, United States; ²Federal University of Itajuba, Brazil</td>
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Jih-Sheng Lai, Bryan Gutierrez, Moonhyun Lee, Chih-Shen Yeh, Hao Wen, Dong Jiao, Zhengming Hou, Hsinche Hsieh
Virginia Polytechnic Institute and State University, United States; Rivian, United States; Transphorm, United States

1875 | Enhanced Interleaved PWM Scheme with Flux Compensation for Three-Parallel Connected Inverters
Chenhui Zhang, Marius Takongmo, John Salmon
University of Alberta, Canada

1828 | A Multi-Level Active Power Filter for Common-Mode Voltage Attenuation in Multi-Level Inverters
Dongwoo Han, Fang Z. Peng, Suman Dwari
Florida State University, United States; Raytheon Technologies Research Center, United States

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Ting Ge, Zichao Ye, Robert C.N. Pilawa-Podgurski
University of California Berkeley, United States

2286 | A Transformerless Composite Step-Down DC-DC Converter with Wide Input Voltage Range
Satyaki Mukherjee, Dragan Maksimović
University of Colorado Boulder, United States

1330 | A Multi-Phase Cascaded Series-Parallel (CaSP) Hybrid Converter for Direct 48 V to Point-of-Load Applications
Yicheng Zhu, Zichao Ye, Ting Ge, Rose Abramson, Robert C.N. Pilawa-Podgurski
University of California Berkeley, United States

2218 | High-Efficiency Operation of a Bidirectional Non-Isolated DC-DC Converter Based on Flying-Capacitor Converters
Kazuaki Tesaki, Makoto Hagiwara
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1747 | A 92.7%-Efficiency 30A 48V-to-1V Dual-Path Hybrid Dickson Converter for PoL Applications
Chen Chen, Jin Liu, Hoi Lee
The University of Texas at Dallas, United States

1868 | A Merged H-Bridge Based Switched Tank Converter for Front-End Voltage Regulator Modules
Jiawei Liang, Haoyu Wang, Hengzhao Yang
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2023 | Analytic Model and Design Procedure of the Single-Secondary Trans-Inductor Voltage Regulator
Hang Shao, Tao Zhao, Dianbo Fu, Daocheng Huang, Jinghai Zhou
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1405 | An Ultrahigh Step-Down DC-DC Converter Based on Switched-Capacitor and Coupled Inductor Techniques  
Longyang Yu¹, Chengzi Yang¹, Wei Mu¹, Fengtao Yang¹, Huaqing Li¹, Laili Wang¹, Yuquan Su², Chi Zhang²  
¹Xi’an Jiaotong University, China; ²MiSiliconn Semiconductor Technologies Co., Ltd., China

1493 | Input-Parallel-Output-Series Two-Stage Interleaved DC-DC Converter Using Coupled Inductors  
Yasuhiro Kodama, Hirotaka Koizumi  
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1651 | Model-Free Predictive Control of Interleaved DC-DC Converters, Based on Ultra-Local Model, with Constant Switching Frequency  
Fernando Bento, Imed Jlassi, Antonio J. Marques Cardoso  
University of Beira Interior, Portugal

1621 | An Efficient and Compact Multi-Port Power Supply for Nano-Satellites  
Arnab Sarkar¹, Nachiketa Deshmukh¹, Pankaj Kumar¹, Sandeep Anand²  
¹Indian Institute of Technology Kanpur, India; ²Indian Institute of Technology Bombay, India

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Vishal Anand A.G.¹, Anirban Pal², Ranganathan Gurunathan¹, Kaushik Basu²  
¹Bloom Energy (I) Pvt Ltd., India; ²Indian Institute of Science, India

2010 | Single-Stage Saturable Inductive-Link Half-Bridge Point of Load Converter  
Tuhin Subhra Sasmal, Kalyan Yenduri, Pritam Das  
Binghamton University, United States

1436 | Multiplexing-Based Flyback Converter for Multi-Port USB Power Delivery with True Power-Sharing  
Xingyue Tian¹, Han Cui¹, Lingxiao Xue²  
¹University of Tennessee Knoxville, United States; ²Oak Ridge National Laboratory, United States

1045 | Transient Model and Elimination Method for DC Bias Current in Dual Active Bridge Converter  
Yangfan Chen, Yu Zhang  
Huazhong University of Science and Technology, China

1220 | Analysis of Synchronous-Rectification Switch Control for Active Class-E Rectifier  
Gwangyol Noh¹, Gyu Cheol Lim², Jung-Ik Ha¹  
¹Seoul National University, Korea; ²Samsun Electronics, Korea; ³Seoul National University Electric Power Research Institute, Korea

1426 | A Modified Three-Port Bidirectional LLC Resonant Converter for Renewable Power Systems  
Xi Chen, Issa Batarseh  
University of Central Florida, United States

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Shandong University, China

1549 | Regenerative Snubber Based Bootstrapped Gate Driver Power Supply for Multiple Input Flyback Converter  
Arnab Sarkar¹, Aditya Aman², Sandeep Anand²  
¹Indian Institute of Technology Kanpur, India; ²Indian Institute of Technology Bombay, India
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| 2362 | **A Boost and LLC Resonant-Based Three-Port DC-DC Converter**  
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| 2479 | **Characteristics of Buck/Boost Operation in an Isolated DC-DC Converter Based on a Phase-Shift Controlled High-Frequency Inverter**  
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| 2074 | **400V-to-48V GaN Modular LLC Resonant Converter with Planar Transformer**  
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¹*The University of Texas at Austin, United States;* ²*Efficient Power Conversion Corporation, United States* |
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Takae Shimada¹, Mizuki Nakahara¹, Takuya Ishigaki²  
¹*Hitachi, Ltd., Japan;* ²*Hitachi Industrial Equipment Systems Co., Ltd., Japan* |
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<td>Xi’an Jiaotong University, China</td>
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1424 | A Bulk-Capacitance Reduction Method Using Self-Driven Thyristor for AC-DC Converters  
Niu Jia¹, Han Cui¹, Lingxiao Xue²  
¹University of Tennessee Knoxville, United States; ²Oak Ridge National Laboratory, United States

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1095 | A Novel Transformerless Common-Ground AC/DC Switching Converter with Integral Fault Protection utilizing Active Isolation Techniques  
Clint Halsted, Madhav Manjrekar, Babak Parkhideh  
University of North Carolina Charlotte, United States

1801 | A Discretized Sampling Based Current Sensorless Control of Single-Phase Totem-Pole Power Factor Corrector  
Ashwin Chandwani¹, Saikat Dey¹, Ayan Mallik¹, Arun Sankar²  
¹Arizona State University, United States; ²Mercedes-Benz R&D North America, Inc., United States

1988 | Design and Implementation of 50V/400A Single-Stage Full-Bridge Synchronous Rectifier for Data Center Application  
Isaac Wong¹, Guangqi Zhu², Birger Pahl³, Subhashish Bhattacharya¹  
¹North Carolina State University, United States; ²Eaton, United States

2038 | Control Techniques for a Current-Mode-Controlled Merged-Energy-Buffer-Based Two-Stage Electrolytic-Free Offline LED Driver  
Maida Farooq¹, Firehiwot Gurara¹, Mauamjeet Khatua¹, Danish Shahzad¹, Saad Pervaiz², Khurram K. Afridi¹  
¹Cornell University, United States; ²Texas Instruments, United States

1862 | Asymmetric Cascaded Transformer Multilevel AC-DC Converter  
Bruna S. Gehrke¹, Cursino B. Jacobina¹, Nayara B. de Freitas², Italo R.F.M.P. da Silva³, Reuben P.R. Sousa¹  
¹Federal University of Campina Grande, Brazil; ²INESC TEC, Portugal; ³Federal University of Paraíba, Brazil

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Mojtaba Forouzesh, Yan-Fei Liu, Paresh C. Sen  
Queen’s University, Canada

1644 | Three-Phase PFC Converter with Reconfigurable LCL Filter  
Jalal Dadkhah¹, Carl N.M. Ho¹, Ken K.M. Siu², River Tin Ho³  
¹University of Manitoba, Canada; ²University of North Texas, United States; ³Hong Kong Applied Science and Technology Research Institute Company Limited, Hong Kong

1583 | Isolated Three-Phase AC to DC Converter with Matrix Converter Applying Compensation for Voltage Error by Voltage-Based Commutation  
Satoshi Nakamura, Hiroki Watanabe, Shunsuke Takuma, Kashin Kiri, Jun-ichi Itoh  
Nagaoka University of Technology, Japan

1843 | Unidirectional Five-Level Rectifiers for WECS Applications  
Amanda P. Monteiro¹, Cursino B. Jacobina¹, Filipe A.C. Bahia², Reuben P.R. Sousa¹  
¹Federal University of Campina Grande, Brazil; ²Federal University of Bahia, Brazil

1215 | A Three-Phase Voltage Doubler Topology Consisted of Small Number of Switching Devices with Low Switching Frequency  
Mizuki Nakahara¹, Hirooki Tokoi¹, Hideto Takada², Hironori Oohashi²  
¹Hitachi, Ltd., Japan; ²Hitachi Industrial Equipment Systems Co., Ltd., Japan
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<td>1873</td>
<td>Surge Voltage Reduction Method for DAB Matrix Converter Using Circulating Current in Whole Load Condition</td>
<td>Shunsuke Takuma, Kashin Kiri, Hiroki Watanabe, Jun-ichi Itoh</td>
<td>Nagaoka University of Technology, Japan</td>
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<td>1976</td>
<td>Ultra-Light Load Performance Enhancement of a 1 MVA SiC Medium Voltage Three Phase Rectifier</td>
<td>Hanning Tang(^1,2), Alex Huang(^1)</td>
<td>(^1)The University of Texas at Austin, United States; (^2)SharkNinja Operating LLC, United States</td>
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<td>2070</td>
<td>A Modular Three-Phase Diode Rectifier with High-Frequency Isolation and Sinusoidal Input Currents</td>
<td>Erick I. Pool-Mazun, Jose Sandoval, Prasad Enjeti</td>
<td>Texas A&amp;M University, United States</td>
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<td>Cheng Huang, Tomoyuki Mannen, Takanori Isobe</td>
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<td>Si Chen(^1), Xinbo Ruan(^1), Ying Li(^2)</td>
<td>(^1)Nanjing University of Aeronautics and Astronautics, China; (^2)University of Nottingham, United Kingdom</td>
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<td>1954</td>
<td>Improved Lifetime of GaN-Based Single Phase PV Inverter Using Dynamic Hardware Allocation</td>
<td>Kamal Sabi, Daniel Costinett</td>
<td>The University of Tennessee Knoxville, United States</td>
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<td>2137</td>
<td>Single-Stage Isolated Half-Bridge/Full-Bridge Converter for DC/AC Applications</td>
<td>Laysa L. Souza(^1), Diego Acevedo-Bueno(^1), Montiê A. Vitorino(^1), Edison R.C. da Silva(^1), Jens Friebe(^2), Antonio M.N. Lima(^1)</td>
<td>(^1)Federal University of Campina Grande, Brazil; (^2)Leibniz Universität Hannover, Germany</td>
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<td>1705</td>
<td>Multilevel Converter Based on Series and Parallel Connections Using High-Frequency Transformer</td>
<td>Filipe V. Rocha(^1), Cursino B. Jacobina(^1), Nady Rocha(^2)</td>
<td>(^1)Federal University of Campina Grande, Brazil; (^2)Federal University of Paraíba, Brazil</td>
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<td>Filipe V. Rocha(^1), Cursino B. Jacobina(^1), Nady Rocha(^2), Antonio de Paula Dias Queiroz(^3)</td>
<td>(^1)Federal University of Campina Grande, Brazil; (^2)Federal University of Paraíba, Brazil; (^3)Federal Institute of Paraíba, Brazil</td>
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Dereje Woldegiorgis, Alan Mantooth  
*University of Arkansas, United States*

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Junhao Luo¹, Khalegh Mozaffari², Brad Lehman¹, Mahshid Amirabadi³  
¹Northeastern University, United States; ²Enphase Energy, United States

1820 | **PUC Converter Based on AC-DC-AC Multilevel Topologies with a Shared Leg**  
Jean T. Cardoso, Cursino B. Jacobina, Phelipe L.S. Rodrigues, Antonio M.N. Lima  
*Federal University of Campina Grande, Brazil*

1087 | **Investigation on Operational Range and Suitable Control for Single Phase to Three Phase Matrix Converter**  
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¹Indian Institute of Technology Delhi, India; ²National Institute of Technology Srinagar, India

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*Federal University of Campina Grande, Brazil*

1457 | **A Single-Phase AC-DC-AC Three-Leg Converter Hybrid with Two and Three Level Legs**  
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¹Federal Institute of Education, Science and Technology of Ceará, Brazil; ²Federal University of Campina Grande, Brazil; ³Federal University of Paraíba, Brazil

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*Federal University of Campina Grande, Brazil*

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*North Carolina State University, United States*
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Samuel C.S. Júnior¹, Cursino Jacobina¹, Edgard L.L. Fabricio²
¹Federal University of Campina Grande, Brazil; ²Federal Institute of Paraíba, Brazil

2100 | A Compact Design Using GaN Semiconductor Devices for a Flying Capacitor Five-Level Inverter
Majid Farhangi¹, Yam P. Siwakoti¹, Reza Barzegarkhoo¹, Saad Ul Hasan¹, Dylan Lu¹, Dan Rogers²
¹University of Technology Sydney, Australia; ²University of Oxford, United Kingdom

1488 | A GaN Based Four-Port Flying Capacitor Multilevel Converter
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Rodrigo P. de Lacerda¹, Cursino B. Jacobina¹, Edgard L.L. Fabricio², Jean Torelli Cardoso¹
¹Federal University of Campina Grande, Brazil; ²Federal Institute of Paraíba, Brazil

1870 | A Seven-Level Inverter with Natural Balance and Boosting Capability
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1815 | Single-Phase AC-DC-AC Multilevel Five-Leg Converter Based on a High-Frequency Transformer
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S. Foti¹, A. Testa¹, S. De Caro¹, G. Scelba², A. Cusumano²
¹University of Messina, Italy; ²University of Catania, Italy

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¹Federal University of Bahia, Brazil; ²Federal University of Paraíba, Brazil

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¹Islamic Azad University, Iran; ²Aalborg University, Denmark

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¹University of Technology Sydney, Australia; ²Newcastle University in Singapore, Singapore

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University of Stuttgart, Germany

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¹Roma Tre University, Italy; ²University of Innsbruck, Austria

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¹Aarhus University, Denmark; ²École de technologie supérieure, Canada

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¹The University of Sheffield, United Kingdom; ²École de Technologie Supérieure, Canada; ³Université Grenoble Alpes, France

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¹University of Messina, Italy; ²University of Catania, Italy
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Hong Li¹, Zexi Zhou¹, Zhipeng Zhang¹, Guoen Cao², Yajing Zhang³
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¹Virginia Polytechnic Institute and State University, United States; ²Oak Ridge National Laboratory, United States

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¹Nanyang Technological University, Singapore; ²Fuzhou University, China; ³Zhejiang University, China

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¹RWTH Aachen University, Germany; ²Silicon Austria Labs GmbH, Austria

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¹University of Washington, United States; ²University of Colorado, United States; ³National Renewable Energy Laboratory, United States

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University of Connecticut, United States

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¹University of Colorado Boulder, United States; ²Advanced Energy Industries, Inc., United States

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¹University of Alberta, Canada; ²Mississippi State University, United States

1962 | A Floquet Theory-Based Stability Analysis Method for Cascaded DC-DC Converters by Combining with the Describing Function of PWM Link
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¹Université Grenoble Alpes, France; ²University of Nottingham, United Kingdom; ³University of Pavia, Italy; ⁴Centrale Lille, France

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¹Consultant, United States; ²Naval Postgraduate School, United States

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Kehu Yang¹, Suna Pan¹, Huawei Li²
¹China University of Mining and Technology, China; ²Beijing Institute of Aerospace Test Technology, China

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¹CentraleSupélec, France, France; ²EnerSys, France

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Georgia Institute of Technology, United States

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National Tsing Hua University, Taiwan

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Georgia Institute of Technology, United States

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¹University of Washington, United States; ²The Pennsylvania State University, United States

1956 | Modelling and Controller Design for Three-Phase Four-Leg Three-Level T-Type Inverter
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¹Temple University, United States; ²Argonne National Laboratory, United States

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Giovanna Oriti¹, Alexander L. Julian², Daniel P. DeToma³
¹Naval Postgraduate School, United States; ²Consultant, United States; ³US Navy, United States

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¹Queen’s University, Canada; ²Voltsafe Inc., Canada

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1860 | Small-Signal Modeling and Output Impedance Analysis of Three Stage Synchronous Generator for More Electric Aircraft
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¹Nanjing University of Aeronautics and Astronautics, China; ²Saanxi Aero Electric Co. Ltd., China

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¹Nanyang Technological University, Singapore; ²Basque Research and Technology Alliance, Spain; ³University of Western Australia, Australia; ⁴Universitat Rovira i Virgili, Spain

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¹Huawei Technologies Düsseldorf GmbH, Germany; ²Christian-Albrechts-Universität zu Kiel, Germany

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¹IETR, France; ²IREENA, France

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¹Wuhan University, China; ²Aalborg University, Denmark

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¹University of Warwick, United Kingdom; ²University of Vigo, Spain

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¹OPAL-RT Corporation, United States; ²Oak Ridge National Laboratory, United States; ³OPAL-RT Technologies, Inc., Canada; ⁴Georgia Institute of Technology, United States

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1University of Houston, United States; 2Tagore Technology, United States

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Zijian Wang, Bowen Jiang, Qiang Wei
Lakehead University, Canada

1499 | Toroidal Inductor Design and Comparison between Interleaved and Non-Interleaved 300 kW High Efficiency SiC Inverter
Harish Suryanarayana1, Maziar Mobarez1, Jacob Miscio1, Xiaoqing Song1, Arun Kadavelugu1, Silvio Colombi2
1ABB, United States; 2ABB, Switzerland

2481 | Minimization of DC-Link Capacitance for a DC-Link Based Variable Speed Constant Frequency Aircraft Power System
Goutham Selvaraj1, Kaushik Rajashekara1, Krishna Raj Ramachandran Potti2
1University of Houston, United States; 2Indian Institute of Technology Delhi, India

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1130 | A Simple Common-Mode Voltage Reduction Method Based on Zero-Sequence Voltage Injection for a Back-to-Back Three-Level NPC Converter
Xiaona Xu, Kui Wang, Zedong Zheng, Yongdong Li
Tsinghua University, China

1157 | Design and Implementation of Bidirectional Voltage-Multiplier Front-End Converter for Switched Reluctance Motor Drive
Hung-Chi Chen, Yu-Jen Lin
National Yang Ming Chiao Tung University, Taiwan

2355 | Efficient Predictive Control Scheme for Optimal Operation of Five Level Four Switch Inverter
Zhanfan Yu, Sally Sajadian
Lafayette College, United States
1662 | A Carried-Based Space Vector Modulation Scheme for Si and SiC Based Enhanced Hybrid-ANPC Converter
Satish Belkhode, Anshuman Shukla, Suryanarayana Doolla
Indian Institute of Technology Bombay, India

1613 | A Commutation Method Free from Inrush Current for the Carrier-Based PWM Controlled Direct Matrix Converter
Sahel Solemanifard, Yan-Xing Chen, Mohammadreza Lak, Tzung-Lin Lee
National Sun Yat-sen University, Taiwan

Session F18: Power Converter Modeling and Control – 2

1128 | Reactive Power Control to Minimize Inductor Current for Single Phase Dual Active Bridge DC/DC Converters
Hamid Naseem, Jul-Ki Seok
Yeungnam University, Korea

1630 | Predictive Control for an Active Magnetic Bearing System with Sensorless Position Control
Luca Tarisciotti¹, Luca Papini², Constanza Ahumada³, Paolo Bolognesi²
¹University Andres Bello, Chile; ²University of Pisa, Italy; ³University of Chile, Chile

1946 | Analysis and Control of Synchronous Rectification for MHz Class-E Resonant Rectifier with Load Variation
Gyu Cheol Lim, Gwangyol Noh, Jung-Ik Ha
Seoul National University, Korea

1723 | Design Oriented Analysis of Control Loops Interaction in Power Synchronization-Based Voltage Source Converter
Federico Cecati¹, Marco Liserre¹, Yicheng Liao², Xiongfei Wang², Frede Blaabjerg²
¹Christian-Albrechts-Universität zu Kiel, Germany; ²Aalborg University, Denmark

2379 | A Novel Modulation Technique for Pulsating DC Link Multistage Converter with Zero Voltage Transition Based on Different and Unrelated Switching Frequencies
Daniele Marciano, Simone Palazzo, Carmine Abbate, Giovanni Busatto, Annunziata Sanseverino, Davide Tedesco, Francesco Velardi
University of Cassino and Southern Lazio, Italy

Session F19: Power Converter Modeling and Control – 3

1266 | A Novel Closed-Form Analytical Model for Zero-Voltage Switching (ZVS) Operation of a Totem Pole PFC with Hysteresis Current Control
Marco Torrisi¹, Sebastiano Messina¹, Mario Cacciato²
¹STMicroelectronics, Italy; ²University of Catania, Italy

1036 | Finite Control Set Model Predictive Control of a DC-DC Boost Converter Ensuring Time-Optimal Regulation and Controlled Output Voltage Deviation
Thibaut Harzig, Brandon Grainger
University of Pittsburgh, United States

1912 | A PWM Control Method for Reducing dv/dt in Cascaded Power Converters
Mahima Gupta
Portland State University, United States

2404 | Dispatchable Virtual-Oscillator-Controlled Inverters with Current-Limiting and MPPT Capabilities
Minghui Lu¹, Rahul Mallik¹, Brian Johnson¹, Sairaj Dhople²
¹University of Washington, United States; ²University of Minnesota, United States
2304 | Small Signal Stability Analysis of Paralleled Grid-Forming Islanded Voltage Regulated Inverters Using Self Synchronization
Peishuo Mu, Brendan McGrath, Donald Grahame Holmes, Carlos Teixeira
RMIT University, Australia

Session F20: Power Converter Modeling and Control – 4

1780 | A Carrier-Based PWM to Achieve Minimum Flux of Coupled Inductor for Interleaved Three-Level Inverters
Ruirui Chen¹, Fred Wang¹,²
¹The University of Tennessee Knoxville, United States; ²Oak Ridge National Laboratory, United States

2305 | Discrete Time Analysis of Dual Loop Stationary Frame Integral Dominant Voltage Regulated Inverters
Haris Siraj, Brendan McGrath, Inam Ullah Nutkani
RMIT University, Australia

1289 | Deep Reinforcement Learning Based Input Voltage Sharing Method for Input-Series Output-Parallel Dual Active Bridge Converter in DC Microgrids
Yu Zeng¹, Ali Maswood¹, Josep Pou¹, Xin Zhang³, Changjiang Sun¹, Zhan Li¹, Suvajit Mukherjee², Amit Kumar Gupta³, Jiaxin Dong¹
¹Nanyang Technology University, Singapore; ²Zhejiang University, China; ³Rolls-Royce@NTU Corporate Lab, Singapore

1343 | An LLC-Resonant Power Decoupling Strategy for a Quad-Active-Bridge Power Channel Based AC/DC Power Electronics Transformer
Xiaohui Li¹, Linqian Cheng¹, Liqun He¹, Chudi Lin¹, Cheng Wang², Zhongkui Zhu¹
¹Soochow University, China; ²Nanjing University of Science and Technology, China

1595 | Zero-Sequence Circulating Current Suppression with Stand-Alone Feedforward Control for Power Hardware-in-the-Loop System
Jun-Hyung Jung, Marius Langwasser, Sante Pugliese, Marco Liserre
Christian-Albrechts-Universität zu Kiel, Germany

Session F21: Power Converter Stability I

2563 | Improved Bilinear Discrete-Time Modeling of the Single-Phase Dual Active Bridge DC-DC Converter
Mohammad Tauquir Iqbal, Ali Iftekhar Maswood, Md Shafquat Ullah Khan, Yu Zeng
Nanyang Technological University, Singapore

1596 | Interaction Analysis of Current Control Loops in MMC under Asymmetrical Grid Faults
Jianglong Che, Meng Huang, Xiaoming Zha, Ju Sheng, Xikun Fu
Wuhan University, China

1184 | Transient Angle Stability Prediction of Virtual Synchronous Generator Using LSTM Neural Network
Yang Shen, Zhikang Shuai, Chao Shen, Xia Shen, Jun Ge
Hunan University, China
1155 | Modeling and Rekasius Substitution Stability Analysis of the Multi-Terminal MMC-HVDC Cyber-Physical System  
Jingming Cao¹, Chaoyu Dong¹², Xiaodan Yu¹, Yunfei Mu¹, Qian Xiao¹, Hongjie Jia¹  
¹Tianjin University, China; ²Imperial College London, United Kingdom

1275 | Region-Based Stability Analysis on DC MGs with Consensus-Based Secondary Control and Communication Delay  
Yuhua Du¹, Yuxi Men¹, Lizhi Ding¹, Xiaonan Lu², Bo Chen², Jianzhe Liu²  
¹Temple University, United States; ²Argonne National Laboratory, United States

Session F22: Control Aspects in Power Electronic Systems – 1

2216 | Distributed Control for Modular Multilevel Converters Operated in Switching-Cycle Balancing Mode  
Boran Fan¹, Jun Wang², Yu Rong¹, Vladimir Mitrovic¹, Jianghui Yu¹, Slavko Mocevic¹, Rolando Burgos¹, Dushan Boroyevich¹  
¹Virginia Polytechnic Institute and State University, United States; ²University of Nebraska Lincoln, United States

1523 | Decoupled Modulation Scheme for Three-Phase Four-Leg Four-Wire Three-Level T-Type Inverter  
Li Zhang, Haoxin Yang, Pengfei Tu, Yi Tang  
Nanyang Technological University, Singapore

1631 | Second-Order Sliding-Mode Controller for Boost Converters with Parameter Estimation  
Celiang Deng¹, Rui Ling¹, Dongxue Li²  
¹Chongqing University, China; ²Vicor Corporation, United States

2369 | Adaptive Pre-Synchronization and Discrete-Time Implementation for Unified Virtual Oscillator Control  
M.A. Awal, Md Rifat Kaisar Rachi, Md Rashed Hassan Bipu, Hui Yu, Iqbal Husain  
North Carolina State University, United States

2502 | An Improved Model-Free Finite Control Set Predictive Power Control for PWM Rectifiers  
Haitao Yang¹, Zeyu Min¹, Yongchang Zhang¹, Zeting Wang¹, Dong Jiang²  
¹North China University of Technology, China; ²Huazhong University of Science and Technology, China

1345 | Feed-Forward Compensation for Model Predictive Control in Tri-Port Current-Source Medium-Voltage String Inverters for PV-Plus-Storage Farms  
Zheng An, Rajendra Prasad Kandula, Deepak Divan  
Georgia Institute of Technology, United States

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1884 | A Control Method Based on Power Factor for Improving Output Voltage Stability and Efficiency of LLC Converter in Wide Range of Output Voltage and Load Impedance  
Kazuhiro Umetani¹, Kota Shimomura¹, Kenta Yamada¹, Taichi Kawakami², Ishihara Masataka¹, Eiji Hiraki¹  
¹Okayama University, Japan; ²Osaka Prefecture University College of Technology, Japan

1110 | An Unequal Power Sharing Strategy for Capacitive- and Inductive-Coupling Inverters in Microgrid  
Wenyang Deng, Qinhao Li, Yongjun Zhang, Yingqi Yi, Guoquan Huang  
South China University of Technology, China

2525 | PWM Control of 3-Phase PFC Vienna Rectifier Derived from an Average Current-Based Control of Single-Phase PFC Boost Converter  
Lotfi Beghou, Miteshkuma Popat, Steven MacDonald  
Leonardo DRS, Canada
2392 | Computation-Cost-Invariant Universal Space-Vector Pulse-Width Modulation for Multilevel Inverters
Fa Chen, Wei Qiao, Hongmei Wang, Liyan Qu
University of Nebraska Lincoln, United States

1640 | Unified Cost Function Model Predictive Control for a Three-Stage Smart Transformer
Luca Tarisciotti1, Giampaolo Buticchi2, Giovanni De Carne3, Yang Jiajun2, Chunyang Gu2, Patrick Wheeler4
1University Andres Bello, Chile; 2University of Nottingham Ningbo, China; 3Karlsruhe Institute of Technology, Germany; 4University of Nottingham, United Kingdom

Session F24: Control Aspects in Power Electronic Systems – 3

2339 | Oversampling Multi-Variable Control for Soft-Switching Solid-State Transformer
Decheng Yan, Aniruddh Marellapudi, Rajendra Prasad Kandula, Deepak Divan
Georgia Institute of Technology, United States

1068 | Complete Time-Delay Compensation Method for LCL-Type Grid-Connected Inverter with Capacitor-Current Active Damping
Shaojie Li, Hua Lin, Xingwei Wang, Ting Hua
Huazhong University of Science and Technology, China

2054 | Noise Mitigation in Control Effort in Three-Phase DC-AC Converters Using LQG/LTR Tracker
Jesus D. Vasquez-Plaza, Juan F. Patarroyo-Montenegro, Andres F. Lopez-Chavarro, Enrique A. Sanabria-Torres, Daniel D. Campo-Ossa, Fabio Andrade
University of Puerto Rico, Puerto Rico

1504 | Output Current Limiting for ON-OFF Controlled Very High Frequency Class E DC-DC Converter
Ying Li, Xinbo Ruan, Si Chen
Nanjing University of Aeronautics and Astronautics, China

1043 | A Constant Current Digital Control Method for Primary-Side Regulation Active-Clamp Flyback Converter in CCM Mode
Chong Wang1, Daying Sun1, Xiang Zhang1, Wenhua Gu1, Sang Gui2
1Nanjing University of Science and Technology, China; 2Wuxi Taclink Optoelectronics Technology Company Limited, China

1800 | A Comparison between Single-Stage and Dual-Stage High-Gain GaN DC/DC Converters for Li-ion Battery Modules
Miguel Crespo1, Pablo Garcia2, Konstantin Edl3, Ramy Georgious4, Cristian Blanco4, Igor Cantero1
1Cegasa, Spain; 2University of Oviedo, Spain; 3IsarAerospace Technologies GmbH, Germany; 4ENFASYS, Spain

Session F25: Modeling and Control in Power Electronic Systems

1811 | Data-Driven Modeling of Power-Electronics-Based Power Systems considering the Operating Points Variation
Mengfan Zhang1, Xiongfei Wang1, Qianwen Xu2
1Aalborg University, Denmark; 2KTH Royal Institute of Technology, Sweden

1858 | Modeling and Impedance Analysis of a Turbine-Generator-Rectifier System with Electromechanical Dynamic Interactions in More Electric Aircraft
Qing Lin1, Bo Wen1, Rolando Burgos1, John Noon2
1Virginia Polytechnic Institute and State University, United States; 2Moog Inc., United States

2265 | Comparison of Anti-Windup Alternatives for Parallel Controllers
Cristina González Moral1, Diego Fernández Laborda2, Juan M. Guerrero Muñoz2, Carlos Rivas1, David Diaz Reigosa2
1Electrotécnica Industrial y Naval S.L., Spain; 2University of Oviedo, Spain
2039 | Adaptive Voltage Positioning Design of Single Stage 48/1V Sigma Converter for Fast Transient Response
Xin Lou¹, Qiang Li¹, Mohamed H. Ahmed²
¹Virginia Polytechnic Institute and State University, United States; ²Texas Instruments, United States

Session F26: Power Converter EMI I

2388 | Common Mode EMI Analysis in Power Electronics Enabled Power System
Ashik Amin, Tahmid Ibne Mannan, Seungdeog Choi
Mississippi State University, United States

1056 | A Fully Integrated CM Choke with Improved DM Noise Attenuation
Shiqi Jiang, Panbao Wang, Wei Wang, Wei Wei, Guihua Liu, Dianguo Xu
Harbin Institute of Technology, China

1205 | Switching Transient Traced Scheme Based on Sinusoidal Waveform to Reduce EMI for IGBTs
Jianan Chen, Dong Jiang, Wei Sun
Huazhong University of Science and Technology, China

1749 | Planar Common-Mode EMI Filter Design and Optimization in a 100-kW SiC-Based Generator-Rectifier System for High-Altitude Operation
Xingchen Zhao, Jiewen Hu, Lakshmi Ravi, Dong Dong, Rolando Burgos
Virginia Polytechnic Institute and State University, United States

Session F27: Converter Power Quality I

2140 | Quantitative Analysis of Third-Harmonic Neutral-Point Current, Its Impacts, and Mitigation in Three-Level NPC Inverters
D. Venkatramanan¹, V. Nimesh², Brian Johnson², Sairaj Dhople¹
¹University of Minnesota, United States; ²University of Washington, United States

2289 | Evaluation of Objective Functions Used in Bio-Inspired Harmonic Optimization Algorithms for Multilevel Converters
Kaiqi Ren, Zhaoyuan Li, Kehu Yang
China University of Mining and Technology, China

1741 | Four-Wire Active Power Filter Based on Asymmetric Cascaded H-Bridges
Samuel C.S. Júnior¹, Cursino Jacobina¹, Edgard L.L. Fabricio²
¹Federal University of Campina Grande, Brazil; ²Federal Institute of Paraíba, Brazil

2262 | Improved LCL Filter Design Procedure for Grid-Connected Voltage-Source Inverter System
Xingyu Yang, Mohammad Alathamneh, R.M. Nelms
Auburn University, United States

Session F28: Reliability, Diagnostics and Fault Analysis of Power Converters – 1

1307 | A Family of Redundant-Switch Configurations for Improving Fault-Tolerant Feature of Power Converters
Tohid Rahimi¹, Hossein Khoun Jahan², Saeed Peyghami³, Ding Lei¹, Frede Blaabjerg³, Pooya Davari³
¹Shandong University, China; ²Azerbaijan Regional Electric Company, Iran; ³Aalborg University, Denmark

1535 | Performance Evaluation of the Multiwinding Redundancy Approach in MTB DC-DC Converters
Thiago Pereira, Felix Hoffmann, Marco Liserre
Christian-Albrechts-Universität zu Kiel, Germany
1762 | **Diagnosing Power Module Degradation with High-Resolution, Data-Driven Methods**
Christoph H. van der Broeck1, Timothy A. Polom2, Rik De Doncker3
1FEV Europe GmbH, Germany; 2Silicon Austria Labs GmbH, Austria; 3RWTH Aachen University, Germany

2102 | **Fault Detection and Management of the Three-Phase 4-Leg Voltage Source Inverter**
Mi Tang1, Pericle Zanchetta1, M. di Benedetto2, A. Lidozzi2, L. Solero2
1University of Nottingham, United Kingdom; 2Roma Tre University, Italy

### Session F29: Reliability, Diagnostics and Fault Analysis of Power Converters – 2

1787 | **Employing the Generative Adversarial Networks (GAN) for Reliability Assessment of Converters**
Amirali Davoodi1, Saeed Peyghami1, Yongheng Yang2, Tomislav Dragičević3, Frede Blaabjerg1
1Aalborg University, Denmark; 2Zhejiang University, China; 3Technical University of Denmark, Denmark

1298 | **Reliability of Wind Turbine Power Modules Using High-Resolution Wind Data Reconstruction: A Digital Twin Concept**
Nikolaos Iosifidis1, Yanghao Zhong1, Borong Hu2, Biyun Chen3, Li Ran1, Subhash Lakshminarayana1, Chunjiang Jia4, Paul McKeever4, Chong Ng5
1University of Warwick, United Kingdom; 2University of Cambridge, United Kingdom; 3Guangxi University, China; 4Offshore Renewable Energy Catapult, United Kingdom

1511 | **A Comparative Study on Converter-Level On-State Voltage Measurement Circuits for Power Semiconductor Devices**
Yingzhou Peng, Huai Wang
Aalborg University, Denmark

1384 | **An Ultra-Fast Short Circuit Protection for Three-Phase GaN Electric Drives**
Darian Verdy Retianza, Luc Spooren, Jeroen van Duivenbode, Henk Huisman
Eindhoven University of Technology, The Netherlands

1788 | **An Application of Feature Engineering and Machine Learning Algorithms on Condition Monitoring of SiC Converters**
Afshin Loghmani Moghaddam Toussi, Amir Sajjad Bahman, Francesco Iannuzzo, Frede Blaabjerg
Aalborg University, Denmark

### Topic G: Electrical Machines

### Session G01: Electric Machines in Transportation

1766 | **Experimental Investigation of a Slotless Skewed Stator with a Composite Winding Layer**
Suzanne Collins, Philip Mellor, Nick Simpson
University of Bristol, United Kingdom

1577 | **Optimization of an IPM Traction Motor considering the Electric Drive Unit System Requirements**
Jonathan Godbehere, Mircea Popescu, Melanie Michon
Motor Design Ltd., United Kingdom

2001 | **Comparison of Candidate Designs and Performance Optimization for an Electric Traction Motor Targeting 50 kW/L Power Density**
Nanjun Tang, Ian P. Brown
Illinois Institute of Technology, United States

2402 | **A Comprehensive Comparison of Concentrated Winding and Distributed Continuous Winding Machine Topologies for Hybrid Electric Vehicles**
Tausif Husain, Cong Ma, Narges Taran, Zhao Wan
BorgWarner Inc., United States
Session G02: Permanent Magnet Machines – 1

1625 | Effect of the Interaction of Different Manufacturing Imperfections on the Unbalanced Radial Forces in a Sub-Fractional HP Single-Phase BLDC Motor
Nejat Saed, Stefan Leitner, Annette Mütze
Graz University of Technology, Austria

2565 | Determination of the Symmetric Short-Circuit Currents of Synchronous Permanent Magnet Machines Using Magnetostatic Flux Maps
Simone Ferrari, Paolo Ragazzo, Gaetano Dilevrano, Gianmario Pellegrino
Politecnico di Torino, Italy

1320 | Cogging Torque Analysis in a Series Hybrid Variable Flux Machine Using Lumped Magnetic Circuits
Dwaipayan Barman, Pragasen Pillay
Concordia University, Canada

2142 | A Generalized Theory to Predict the Torque Harmonics in Permanent Magnet Machines
Anant K. Singh\textsuperscript{1}, Ramakrishnan Raja\textsuperscript{1}, Tomy Sebastian\textsuperscript{1}, Kaushik Rajashekar\textsuperscript{2}
\textsuperscript{1}Halla Mechatronics, United States; \textsuperscript{2}University of Houston, United States

Session G03: Modeling of Electric Machines – 1

2363 | The DQ-Theta Flux Map Model of Synchronous Machines
Simone Ferrari, Gaetano Dilevrano, Paolo Ragazzo, Gianmario Pellegrino
Politecnico di Torino, Italy

2272 | Fast Flux Mapping of PM and Synchronous Reluctance Machines: Method Description and Comparison with Full FEA Approach
Matteo Carbonieri\textsuperscript{1}, Wen L. Soong\textsuperscript{2}, Amin Mahmoudi\textsuperscript{3}, Nicola Bianchi\textsuperscript{1}
\textsuperscript{1}University of Padova, Italy; \textsuperscript{2}University of Adelaide, Australia; \textsuperscript{3}Flinders University, Australia

1165 | Optimization of a Line-Start Motor for Centrifugal Loads within Premium Efficiency According to IEC Standard
Diego Troncon\textsuperscript{1}, Luigi Alberti\textsuperscript{1}, Leone Donazzan\textsuperscript{2}, Mauro Daneluzzi\textsuperscript{2}, Massimo Trova\textsuperscript{2}
\textsuperscript{1}University of Padova, Italy; \textsuperscript{2}Orange1 Spa, Italy

1947 | Optimum Design Study on HEFSM Using Variably Magnetizable PM with Low L/D Ratio and Novel PM Arrangement for EV/HEV Traction Applications
Takeshi Okada, Mitsuru Saito, Takashi Kosaka, Hiroaki Matsumori, Nobuyuki Matsui
Nagoya Institute of Technology, Japan

Session G04: Permanent Magnet Machines – 2

1411 | Recent Advances in Analysis and Design of Axial Flux Permanent Magnet Electric Machines
F.N.U. Nishanth\textsuperscript{1}, Joachim Van Verdegem\textsuperscript{2}, Eric L. Severson\textsuperscript{1}
\textsuperscript{1}University of Wisconsin Madison, United States; \textsuperscript{2}Université catholique de Louvain, Belgium

2085 | Investigation of Asymmetric Axial-Flux Hybrid Excited Electrodynamic Wheels for Maglev Transportation
Wei Qin\textsuperscript{1}, Ma Yuhua\textsuperscript{2}, Lv Gang\textsuperscript{1}, Wang Fuyao\textsuperscript{1}, Song Chengrui\textsuperscript{1}, Zhang Jielong\textsuperscript{1}
\textsuperscript{1}Beijing Jiaotong University, China; \textsuperscript{2}Taiyuan Institute of China Coal Technology and Engineering Group, China

2372 | A Survey on the Design and Analysis of Magnetic Screws
Doha Mostafa, Hussain A. Hussain
Kuwait University, Kuwait
Examination for a Hybrid Excitation Motor with Reverse Saliency while Having a Field Winding on a Rotor
Ryusyo Nakazawa¹, Masatsugu Takemoto², Satoshi Ogasawara¹, Koji Orikawa²
¹Hokkaido University, Japan; ²Okayama University, Japan

Session G05: Electric Machines: Fault Analysis

Impact of Static and Dynamic Eccentricity on the Performance of Permanent Magnet Synchronous Machines with Modular Stator Core
Danilo Riquelme¹, Werner Jara¹, Carlos Madariaga², Juan Tapia³, Gerd Bramerdorfer¹, Javier Riedemann⁴
¹Pontificia Universidad Católica de Valparaiso, Chile; ²University of Concepcion, Chile; ³Johannes Kepler University Linz, Austria; ⁴The University of Sheffield, United Kingdom

Performance of Dual Wound Synchronous Reluctance Machines for High Performance Applications considering Winding Faults
Mazharul Chowdhury¹, Mohammad Islam², Iqbal Husain¹
¹North Carolina State University, United States; ²Halla Mechatronics, United States

Test Metrics and Damage Fingerprints in Multistranded Compressed Aluminium Windings
Dominic North, Joshua Hoole, Nick Simpson, Philip Mellor
University of Bristol, United Kingdom

An Investigation into the Diagnosis of Interturn Winding Faults in a Scaled-Down DFIG Using the MCSA and DWT of the Stator and Rotor Current
Ester Hamatwi, Paul Barendse, Azeem Khan
University of Cape Town, South Africa

Development of a Test Rig for Fault Studies on a Scaled-Down DFIG
Ester Hamatwi, Paul Barendse, Azeem Khan
University of Cape Town, South Africa

Session G06: Thermal Analysis of Electric Machines

Model Calibration of Oil Jet and Oil Spray Cooling in Electrical Machines with Hairpin Windings
Chuan Liu¹, Yew Chuan Chong², Melanie Michon², James Goss², David Gerada¹, Zeyuan Xu¹, Chris Gerada¹, He Zhang³
¹University of Nottingham, United Kingdom; ²Motor Design Ltd., United Kingdom; ³University of Nottingham Ningbo, China

Approaches for Improving Lumped Parameter Thermal Networks for Outer Rotor SPM Machines
Daniel Wöckinger¹, Gerd Bramerdorfer¹, Silvio Vascchetto², Andrea Cavagnino², Alberto Tenconi², Wolfgang Amrhein¹, Frank Jeske³
¹Johannes Kepler University Linz, Austria; ²Politecnico di Torino, Italy; ³Ebm-papst St. Georgen Gmbh & Co. KG, Germany

Electromagnetic and Thermal Evaluation of Surface-Mounted PM Vernier Machines
Mostafa Ahmadi Darmani¹, Silvio Vascchetto¹, Andrea Cavagnino¹, Mircea Popescu²
¹Politecnico di Torino, Italy; ²Motor Design Ltd., United Kingdom

Comparison of Superposition Equivalent Loading Methods for Induction Machine Temperature Tests
Silvio Vascchetto¹, Emmanuel Agamloh², Federica Graffeo¹, Andrea Cavagnino¹
¹Politecnico di Torino, Italy; ²Baylor University, United States
### Session G07: Electric Machines Materials and Additive Manufacturing

**1771 | Additive Manufacturing of a Conformal Hybrid-Strand Concentrated Winding Topology for Minimal AC Loss in Electrical Machines**
Nick Simpson¹, Jakob Jung², Axel Helm², Phil Mellor¹
¹University of Bristol, United Kingdom; ²Additive Drives GmbH, Germany

**1934 | Multi-Permeability Optimization Approach for the Iron Core of a Synchronous Reluctance Machine – an Application of Additive Manufacturing**
Thang Q. Pham, Shanelle N. Foster
Michigan State University, United States

**1650 | Selection of Soft Magnetic Composite Material for Electrical Machines Using 3D FEA Simulations**
Maged Ibrahim¹, Sumeet Singh², Dwaipayan Barman², Fabrice Bernier¹, Jean-Michel Lamarre¹, Serge Grenier³, Pragasen Pillay²
¹National Research Council of Canada, Canada; ²Concordia University, Canada; ³Rio Tinto Fer et Titane, Canada

**1722 | Flux Switching Permanent Magnet Motor with Metal Amorphous Nanocomposite Soft Magnetic Material and Rare Earth Free Permanent Magnets**
Satoru Simizu¹, Kevin Byerly¹, Kyle Schneider¹, Heonyoung Kim², Mark Nations², Sneha Narasimhan², Richard Bedingfield², Subhashish Bhattacharya², Michael E. McHenry¹
¹Carnegie Mellon University, United States; ²North Carolina State University, United States

### Session G08: Loss Analysis in Electric Machines

**1048 | Experimental Determination of Conductor Lay and Impact on AC Loss in Volume Manufactured Machines Using X-Ray Computed Tomography**
Joshua Hoole¹, Nick Simpson¹, Philip H. Mellor¹, Abdeljalil Daanoune²
¹University of Bristol, United Kingdom; ²hofer powertrain UK Ltd., United Kingdom

**1645 | Loss Mitigation Techniques for a Novel Toroidal Permanent Magnet Motor**
Maged Ibrahim, Fabrice Bernier, Jean-Michel Lamarre
National Research Council of Canada, Canada

**1708 | Computationally Efficient Prediction of Statistical Variance in the AC Losses of Multi-Stranded Windings**
Philip Mellor, Joshua Hoole, Nick Simpson
University of Bristol, United Kingdom

**1802 | Experimental Evaluation of Iron Losses in Radial Flux Permanent Magnet Synchronous Machines**
Gereon Goldbeck, Gerd Bramerdorfer, Daniel Wöckinger, Christoph Dobler, Wolfgang Amrhein
Johannes Kepler University Linz, Austria

### Session G09: Noise and Vibrations in Electric Machines

**1809 | Effects of Stator Laminations on Acoustic Noise of Electrical Machines**
Issah Ibrahim, David Alister Lowther
McGill University, Canada

**1673 | Analysis of Claw Deflections and Radial Magnetic Forces in Low-Cost Sub-Fractional Horsepower BLDC Claw-Pole Motors**
Stefan Leitner, Nejat Saed, Annette Mütze
Graz University of Technology, Austria
Dominant Spatial Order Airgap Force Based Current Profiling Coupled with Fast Vibration Prediction in Switched Reluctance Machines for NVH Mitigation
Shuvajit Das¹, Md Ehsanul Haque¹, Anik Chowdhury¹, Yilmaz Sozer¹, David Colavincenzo², Fernando Venegas³, Jeffrey Geither²
¹The University of Akron, United States; ²Bendix Commercial Vehicle Systems, United States

Direct Acceleration Harmonic Control with Current Harmonics Injection Method to Reduce Acoustic Noise and Vibration in Switched Reluctance Machines
Omer Gundogmus, Shuvajit Das, Anik Chowdhury, Abdul Wahab Bandarkar, Yilmaz Sozer
The University of Akron, United States

Normalized Analytical Model of Stresses in a Surface Mounted Permanent Magnet Rotor
Martin Johnson, Kyle Hanson, Eric L. Severson
University of Wisconsin Madison, United States

Requirements for Full Passive Suspension on a Bearingless Motor with Electrodynamic Axial Stabilization and Radial Permanent Magnet Bearings
Guilherme Cavalcante Rubio, Yusuke Fujii, Akira Chiba
Tokyo Institute of Technology, Japan

Principles and Test Result of Novel Full Passive Magnetic Levitation Motor with Diamagnetic Disk
Yoshiki Ozawa¹, Yusuke Fujii¹, Akira Chiba¹, Hiroya Sugimoto², Haruhiko Suzuki³, Hannes Bleuler⁴
¹Tokyo Institute of Technology, Japan; ²Tokyo Denki University, Japan; ³Fukushima College, Japan; ⁴EPFL, Switzerland

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¹The University of Manchester, United Kingdom; ²IMRA Europe SAS UK Research Centre, United Kingdom; ³Aisin Seki Co., Ltd., Japan

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¹Huazhong University of Science and Technology, China; ²Universidad Andres Bello, Chile
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\(^1\)Illinois Institute of Technology, United States; \(^2\)EEMA US Inc., United States

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¹Korea University, Korea; ²Incheon National University, Korea; ³Iris Power - A Qualitrol Company, Canada

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¹Seoul National University, Korea; ²Plecko Co, Ltd., Korea

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¹Aalborg University, Denmark; ²KK Wind Solutions, Denmark

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¹Southeast University - Nanjing, China; ²Southeast University - Yancheng, China
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#### Topic I: Power Semiconductor Devices

**Session I01: Power Module and Integration – In Memory of Prof. Braham Ferreira**

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<td>Asif Imran Emon¹, Mustafeez Ul Hassan¹, Abdul Basit Mirza¹, Zhao Yuan², Fang Luo¹</td>
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¹STMicroelectronics, Italy; ²University of Catania, Italy

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¹Advent Diamond, Inc., United States; ²Arizona State University, United States

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¹Zhejiang University, China; ²Eaton, China

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¹Cornell University, United States; ²Lawrence Berkeley National Laboratory, United States

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¹North China Institute of Aerospace Engineering, China; ²North China Electric Power University, China; ³Beijing Information Science and Technology University, China; ⁴BorgWarner Noblesville Technical Center, United States

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¹Eaton, United States; ²North Carolina State University, United States

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¹University of Auckland, New Zealand; ²Georgia Institute of Technology, United States; ³Fisher and Paykel Healthcare, New Zealand

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Yarui Peng, University of Arkansas, USA

I11 | Semiconductor Devices
Francesco Iannuzzo, Aalborg University, Denmark

I12 | Thermal Management
Adam Skorek, Université du Québec à Trois Rivières, Canada

K01 | Wireless Power Transfer – 1
Mehdi Narimani, McMaster University, Canada

K02 | Measurements and Testing
Ariya Sangwongwanich, Aalborg University, Denmark

K03 | Emerging Technologies and Applications – 1
Huai Wang, Aalborg University, Denmark

K04 | Wireless Power Transfer – 2
Mahshid Amirabadi, Northeastern University, USA

K05 | Wireless Power Transfer – 3
Prasad Rajendra Kandula, Georgia Institute of Technology, USA

K06 | Energy Harvesting
Huai Wang, Aalborg University, Denmark

K07 | Measurements and Testing
Ariya Sangwongwanich, Aalborg University, Denmark

K08 | Emerging Technologies and Applications – 2
Harish Sarma Krishnamoorthy, University of Houston, USA

K09 | Emerging Technologies and Applications – 3
Harish Sarma Krishnamoorthy, University of Houston, USA
## Product Sessions

These half-hour, industry-driven sessions, provide an in-depth look off the show floor from our exhibitors, showcasing their innovative products and services.

<table>
<thead>
<tr>
<th>Time</th>
<th>Session</th>
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<tbody>
<tr>
<td>8:30AM-9:00 AM</td>
<td><strong>Low Voltage Ride Through (LVDT) Capability of Solar Energy Inverters Using an FPGA-based Real-Time Simulator</strong></td>
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<td></td>
<td>This Hardware-in-the-Loop demo consists of an inverter controller for grid-connected solar energies and an OP4510 real-time simulator. The controller, developed by the GREENLAB of Hefei University of Technology, is capable of low-voltage ride through—while the Electric Hardware Solver (eHS) is running on the OP4510, and simulates the entire grid-connected solar-energy inverter in real-time on the FPGA at a time step of less than 250 nanoseconds. The design of circuit schematic is done using the new OPAL-RT Schematic Editor. The model receives the PWM gating signals from external controller via digital inputs, and feeds the voltage and current measurement to controller via analog outputs. Also, the HIL model can perform soft-start, soft-stop or soft-reset, as well as update the PID parameters of the external controller via RS232 protocol. Therefore, users can conduct numerous tests automatically using API scripts. Particularly, the scenario features allows the user to pre-define different levels of voltage-sag or the short-circuit level of the host grid for the requirement of low voltage ride through (LVDT) tests.</td>
</tr>
<tr>
<td>9:00AM-9:30 AM</td>
<td><strong>Evolution of Power Delivery for Cloud Computing</strong></td>
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<tr>
<td></td>
<td>New architectures and technologies are increasing power density and efficiency reducing TCO (Total Cost of Ownership.) From AC-DC to the Point of Load, we will look at the power delivery topologies leveraging ST’s unique portfolio, the most complete in the market, with the latest innovations in GaN transistors and drivers.</td>
</tr>
<tr>
<td>3:30PM-4:00PM</td>
<td><strong>G3R™ SiC MOSFETs, Unparalleled Performance and Robustness</strong></td>
</tr>
<tr>
<td></td>
<td>High-efficiency energy usage has become a critical deliverable in next-generation power converters and silicon carbide (SiC) power devices continue to be the key components driving this revolution. In this session, GeneSiC will discuss the superiority of its third-generation (G3R™) SiC MOSFETs, including the performance advantages, design considerations as well as the importance of quality and reliability in demanding applications.</td>
</tr>
</tbody>
</table>
Student Demonstrations provide an opportunity for students from various universities and countries to showcase their emerging technology research outcomes and interact with academia and industry.

Hardware Competition

High Power Density GaN-Based Online Uninterruptible Power Supply
Maida Farooq, Danish Shahzad, Prof. Khurram Afridi
Cornell University, USA

High Power Density GaN-Based Online Uninterruptible Power Supply
Mausamjeet Khatua, Prof. Khurram Afridi
Cornell University, USA

Demonstration of Power Sink with Wide Power-Bandwidth to Emulate Residential Loads
Hitesh Kumar, Prof. Santanu K. Mishra
Indian Institute of Technology Kanpur, India

Common Mode EMI Analysis in Power Electronics Enabled Power System
Ashik Amin, Tahmid Ibne Mannan, Prof. Seungdeog Choi
Mississippi State University, USA

Machine Learning Methods and Open-Source Database for Magnetic Core Loss Modeling
Haoran Li, Mian Liao, Shukai Wang, Prof. Minjie Chen
Princeton University, USA

Vertical-Stacked Liquid-Cooled 48 V-1 V CPU Voltage Regulator with Extreme Power Density
Youssef Elasser, Ping Wang, Prof. Minjie Chen, Dr. Jaeil Baek
Princeton University, USA

A 700kHz 800V/14V GaN-based Isolated DC-DC Converter with Optimized Integrated Transformer for Electrical Vehicles
Huu Phuc Kieu, Donghan Lee, Prof. Sewan Choi
Seoul National University of Science and Technology, South Korea

Blockchain-Enabled Security Module for Firmware Security-Enhanced Smart Inverter
BoHyun Ahn, Seerin Ahmad, Swathi Pedavalli, Prof. Taesic Kim
Texas A&M University-Kingsville, USA

PowerSynth an MCPMs layout Optimization Tool
Quang Le, Imam Al Razi, Tristan Evans, Prof. Alan Mantooth, Prof. Yarui Peng
University of Arkansas, USA

Active Life Balancing to Condition Li-ion Battery Packs
Marium Rasheed, Prof. Regan Zane
Utah State University, USA

Use of 3D Printing Technology to Improve the Voltage Distribution Across Inductor’s Turns
Faisal Alsaiif, Prof. Jin Wang
The Ohio State University, USA

Designing of Flying Capacitor (FC) Module based on GaN Chip
Pasan Gunawardena, Xuesong Wu, Prof. Yunwei Li
University of Alberta, Canada

High-Voltage, High-Current Pulse Generator
Xin Zan, Prof. Al-Thaddeus Avestruz
University of Michigan, Ann Arbor, USA

High Performance Integrated Motor Drive using WBG-enabled Current-Source Converters
Renato Amorim Torres, Hang Dai, Prof. Bulent Sarlioglu, Prof. Thomas Jahns
University of Wisconsin-Madison, USA

Composite Hybrid Energy Storage System
Marium Rasheed, Josh Larsen, Prof. Regan Zane
Utah State University, USA

Software Competition

Demonstration of Power Sink with Wide Power-Bandwidth to Emulate Residential Loads
Hitesh Kumar, Prof. Santanu K. Mishra
Indian Institute of Technology Kanpur, India

Machine Learning Methods and Open-Source Database for Magnetic Core Loss Modeling
Haoran Li, Mian Liao, Shukai Wang, Prof. Minjie Chen
Princeton University, USA

Blockchain-Enabled Security Module for Firmware Security-Enhanced Smart Inverter
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Exhibitor Listing

Allegro MicroSystems
Bodo’s Power Systems
Efficient Power Conversion Corporation
EGSTON Power Electronics
FREEDM Systems Center
GanPower International
GeneSiC
GMW Associates
Halla Mechatronics
How2Power.com
HVR Advanced Power Components Inc.
IEEE Future Networks
IEEE - Industrial Application Society
IEEE - Power Electronics Society
Infineon
Magna-Power
Magnetics
MathWorks
MDPI - Machines
OPAL RT
Payton America Inc
Picotest
Plexim, Inc
Powersim
Richardson RFPD
Rohde & Schwarz USA
Sanrex Corporation
STMicroelectronics
Taiwan Semiconductor Inc.
Vicor
Voltx.ai
Wiley
Wolfspeed
Call for Papers

The Fourteenth Annual IEEE Energy Conversion Congress and Exposition (ECCE 2022) will be held in Detroit, Michigan, USA, from October 9 to October 13, 2022. ECCE is a pivotal international event on energy conversion. ECCE 2022 will feature both industry-driven and application-oriented technical sessions as well as an exposition. The conference will bring together practicing engineers, researchers and other professionals for interactive and multidisciplinary discussions on the latest advances in areas related to energy conversion.

Technical papers are solicited on any subject pertaining to the scope of the conference including, but not limited to, the following major topics:

**Energy Conversion Systems & Applications**
- High power/voltage power conversion
- High voltage isolation techniques
- Energy harvesting
- Energy conversion for information technology and communication systems
- Energy efficiency for residential, commercial, and industrial applications
- Big data and artificial intelligence in energy conversion
- Renewable and alternative energy power electronic systems
- Smart grids, microgrids, and utility applications (HVDC, FACTS, and Solid State Transformers)
- Electrical energy storage
- Wireless power transfer

**Component, Converter & Subsystem Technologies**
- Power electronic devices (silicon and wide bandgap) and applications
- Passive components and materials
- Power electronic packaging integration
- Reliability, advanced fault protection systems, diagnostics, prognostics, and health management
- Thermal management and advanced cooling technologies
- Electromagnetic interference and electromagnetic compatibility
- Power conversion topologies, modulation, and control
- Electrical drive systems and topologies and their control
- Rotating/linear electromechanical devices
- Enabling technologies for Industry 4.0: advanced manufacturing, additive manufacturing, digital twins, cloud design, big data analytics

**Digest Submission:** Prospective authors are requested to submit a single column, single spaced digest no longer than five (5) pages summarizing the proposed paper. The digest should include key equations, figures, tables, and references as appropriate, but no author names or affiliations. Digests not conforming to these requirements will be rejected without review. The digests must clearly state the objectives of the work, its significance in advancing the state of the art, and the methods and specific results in sufficient detail. All digests will go through a double-blind peer review process to ensure a confidential and fair review. The papers presented at the conference will be included in the IEEE Xplore Digital Library. Please refer to the conference website for a detailed list of technical topics and the digest submission method.
Call for Tutorials

The 14th Annual IEEE Energy Conversion Congress and Exposition (ECCE 2022) will be held in Detroit, Michigan, USA, from October 9 to October 13, 2022. ECCE is a pivotal international event on energy conversion. It will bring together practicing engineers, researchers, and other professionals for interactive discussions on the latest advances in areas related to energy conversion.

The ECCE organizing committee invites proposals for tutorials to be presented at ECCE 2022. Each tutorial is 3 hours long, excluding break times. Each accepted tutorial will receive one conference registration together with an honorarium of $1,000. Presenters potentially may be expected to present both in-person and pre-recorded.

Please note that publication of a technical paper will still require a paid full registration. All tutorial proposals should be submitted via the ECCE 2022 web portal under “Call for Tutorials”. Please follow the Tutorial Proposal Form on the website as a submission template. The proposals will be reviewed by a panel of subject matter experts.

One or more of the following elements are strongly encouraged in the tutorial proposals:

A. Industry led or co-hosted lectures;
B. Interactive instructor-audience approaches, including hands-on demonstrations and practices;
C. Application focused session on tools or methods for the practicing engineer.

Tutorials considered to be less attractive to the audience are:

a) Topics that are too narrowly focused;
b) Lectures that are not balanced between theory and application;
c) Tutorial topics or teams presented previously in immediate past ECCE or other major IAS/PELS conferences;
d) Tutorials that narrowly focus on presenter’s own research works that are already publicly available

e) Solicitation of a particular product or service.

Potential topic areas include but are not limited to:

Energy Conversion Systems and Applications

- Renewable energy, including under-represented ocean-wave, tidal, geothermal
- Smart grids, micro-grids, nano-grids
- Electrical energy storage, including real physics or controlled virtual storage
- Energy conversion for information technology and communication systems
- Energy harvesting and conversion
- Smart and energy efficient buildings
- Energy efficiency for advanced manufacturing
- Big data and machine learning in energy conversion
- Cybersecurity in energy conversion systems
- Transportation electrification, including aircraft and urban aerial mobility
- Battery charging technologies
- Resiliency in energy systems

Component, Converter and Subsystem Technologies

- Power electronic devices
- Power conversion topologies, modeling, and control
- Electric machines and drives
- Passive components, magnetics, and materials
- Packaging, integration, and advanced manufacturing
- EMI and EMC
- Thermal management, advanced cooling technologies
- Wireless power transfer
- High voltage power conversion, including insulation technologies
- Design automation or optimization
- Reliability, diagnostics, prognostics, and health management
- Fault-tolerant converters and systems

Others

- Pedagogy for undergraduate learning or under-represented groups
- Post-COVID technology innovations
- Entrepreneurship, technology transfer, business management
- Use of standards for specific applications

IMPORTANT DATES

February 11, 2022
Tutorial proposal due

April 8, 2022
Notification of acceptance

July 1, 2022
Full tutorials materials due
Format: Maximum 5 pages. All pages are formatted to 8-1/2 by 11 inch or A4 paper with margins of one inch on every side. All texts use single space, Times New Roman, black ink, and a font size of 11 or 12.

Recommended Sections:

1. Tutorial Title

2. Instructor Team
   Name(s), affiliation(s), and contact information

3. Abstract
   No more than 500 words. Accepted abstract will be published through the conference website, program, and proceedings.

4. Tutorial Outline
   Outline shall only define the topics and subtopics. No detailed descriptions please. Time allocation and instructor breakdown by topics is recommended.

5. Lecture Style and Requirements
   Briefly describe the tutorial format, which may include traditional lecture, software/hardware demonstration, interactive audience polls/quizzes, worksheets, discussion, etc. Note any equipment or space requirements beyond a laptop and projector. Also list the targeted audience and tutorial difficulty level, including any pre-requisite knowledge.

6. Instructor Biography
   No more than 200 words for each person. Each biography shall include the qualifications most relevant to the proposal. Past tutorial/teaching experience and outcome can be highlighted. External website link can be included but may not be reviewed.
The 14th Annual IEEE Energy Conversion Congress and Exposition (ECCE 2022) will be held in Detroit, Michigan, USA, from October 9 to October 13, 2022. Special Sessions are solicited focusing on emerging technologies and industry-oriented topics. Industry and government organizations or speakers are of particular interest. Guest speakers will be invited on the day their session is scheduled. No written papers are required. Materials presented in the Special Sessions will not be included in the conference proceedings. Each session will be assigned either one or two 100-minute slot(s), subject to conference program scheduling.

Different session formats are solicited: 1) Formal presentations; 2) Informal talks with or without slides; 3) Full Q&A panel; 4) Debate; 5) Other creative or hybrid styles.

One or more of the following elements are strongly encouraged in the special session proposals: A) Significant industry or government involvement; B) Industrial application oriented; C) ECCE 2022 regionally oriented topics; D) Collaborative cross-disciplinary topics or teams; E) Creative formats that engage the audience, especially industry.

Factors considered as less attractive to the audience are: a) Non-emerging topics; b) Academic lectures; c) Similar teams with similar topics from the immediate past ECCE; d) Solicitation of a particular product or service; e) Unclear plans including unconfirmed speakers.

Potential topic areas include but are not limited to:

**Energy Conversion Systems and Applications**
- Transportation electrification, including EV, trucks, aircraft, UAV, trains, ships
- Energy storage systems, including real or virtual storage
- Charging stations, vehicle to grid
- Additive manufacturing
- Renewable energy integration
- Smart grids, micro grids, nano grids
- Resiliency in energy systems
- Smart and energy efficient buildings
- Energy conversion for information technology
- Big data and machine learning in energy conversion
- Cybersecurity in energy conversion
- Design automation and optimization

**Components, Converters, and Subsystems**
- Power semiconductor devices, magnetics, capacitances
- Power conversion topologies, modeling, and control
- Electric machines and drives
- Packaging, integration, and advanced manufacturing
- EMI and EMC
- Thermal management, advanced cooling technologies
- Wireless power transfer
- High voltage power conversion, including insulation technologies
- Reliability, diagnostics, prognostics, and health management

**Others**
- Advanced testing and validation, including demo
- Standards development
- Education and career development
- Entrepreneurship, technology transfer, business management
- Post-COVID technology innovations

**Proposal Submission Guidelines:** All special session proposals must be submitted via the ECCE 2022 web portal under “Call for Special Sessions”. Please follow the Proposal Form on the website as a submission template. The proposals will be reviewed by a panel of subject matter experts.
**Format:** Maximum 5 pages. All pages are formatted to 8.5x11” or A4 paper with margins of one inch on every side. All texts use single space, Times New Roman, and a font size of 11 or 12. A Word template will be posted on the official website under Call for Special Sessions.

**Recommended Sections:**

1. **Special Session Title**

2. **Proposed Session Format**
   - [ ] Formal presentations
   - [ ] Informal talks
   - [ ] Full Q&A panel
   - [ ] Debate
   - [ ] Create own style – see Call For Proposal.
   
   Describe the format at a high level, and note any creative activities such as software/hardware demonstration, virtual tours, interactive audience polls, etc.

3. **Proposed Timing**
   - [ ] 100 minutes
   - [ ] 2x100 minutes

4. **Session Organizers**
   
   List name(s), title(s), affiliation(s), and email(s).

5. **Session Speakers/Panelists**
   
   List names, titles, and affiliations. Clearly note each speaker’s availability: choose “confirmed” or “tentative”; failure to do so will be treated as all tentative.

6. **Abstract**
   
   No more than 500 words. Accepted abstract will be published through the conference website and program book.

7. **Session Outline**
   
   Only list the proposed topics/titles/activities. No detailed descriptions necessary. Indicate time allocation and speaker breakdown, if possible.

8. **Organizer Biography**
   
   No more than 200 words for each person. External website link can be included but may not be reviewed.

9. **Speaker/Panelist Biography**
   
   No more than 200 words for each person. External website link can be included but may not be reviewed.